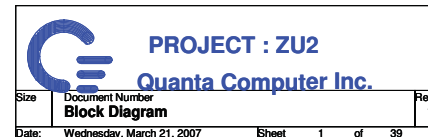
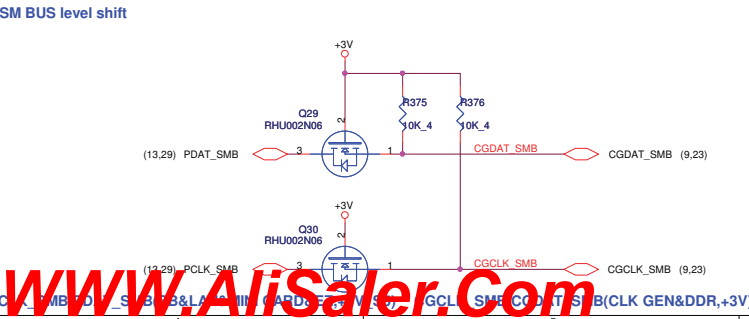
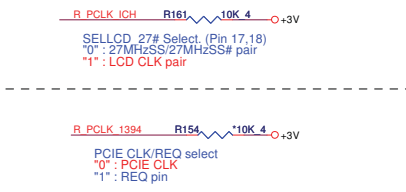
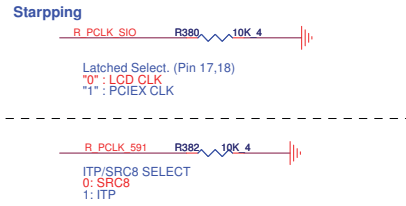
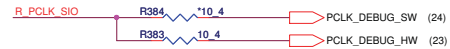
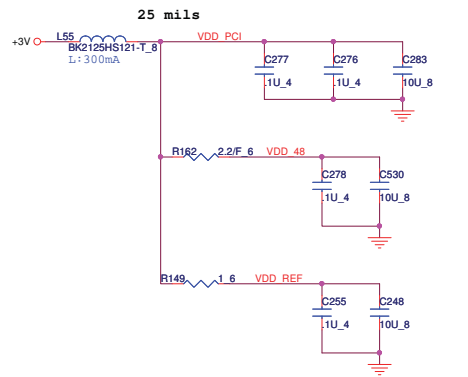


PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts	CLOCK
CB1410	AD17	REQ0# / GNT0#	INTE#	CK410/PCI3
TIAB23	AD25	REQ2# / GNT2#	INTF#	CK410/PCI8
MRS10	AD18	REQ1# / GNT1#	INTG#	CK410/PCI4

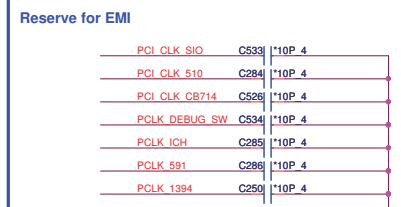
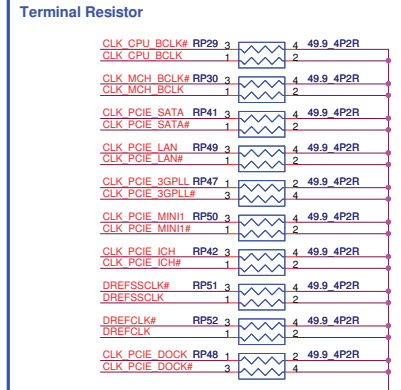
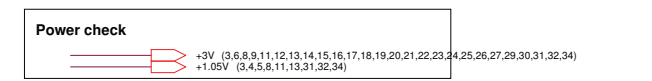
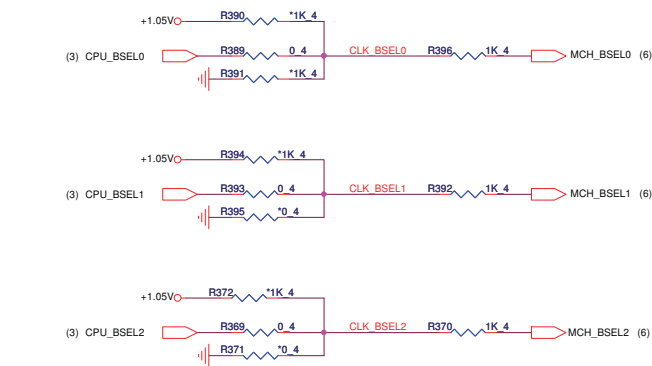




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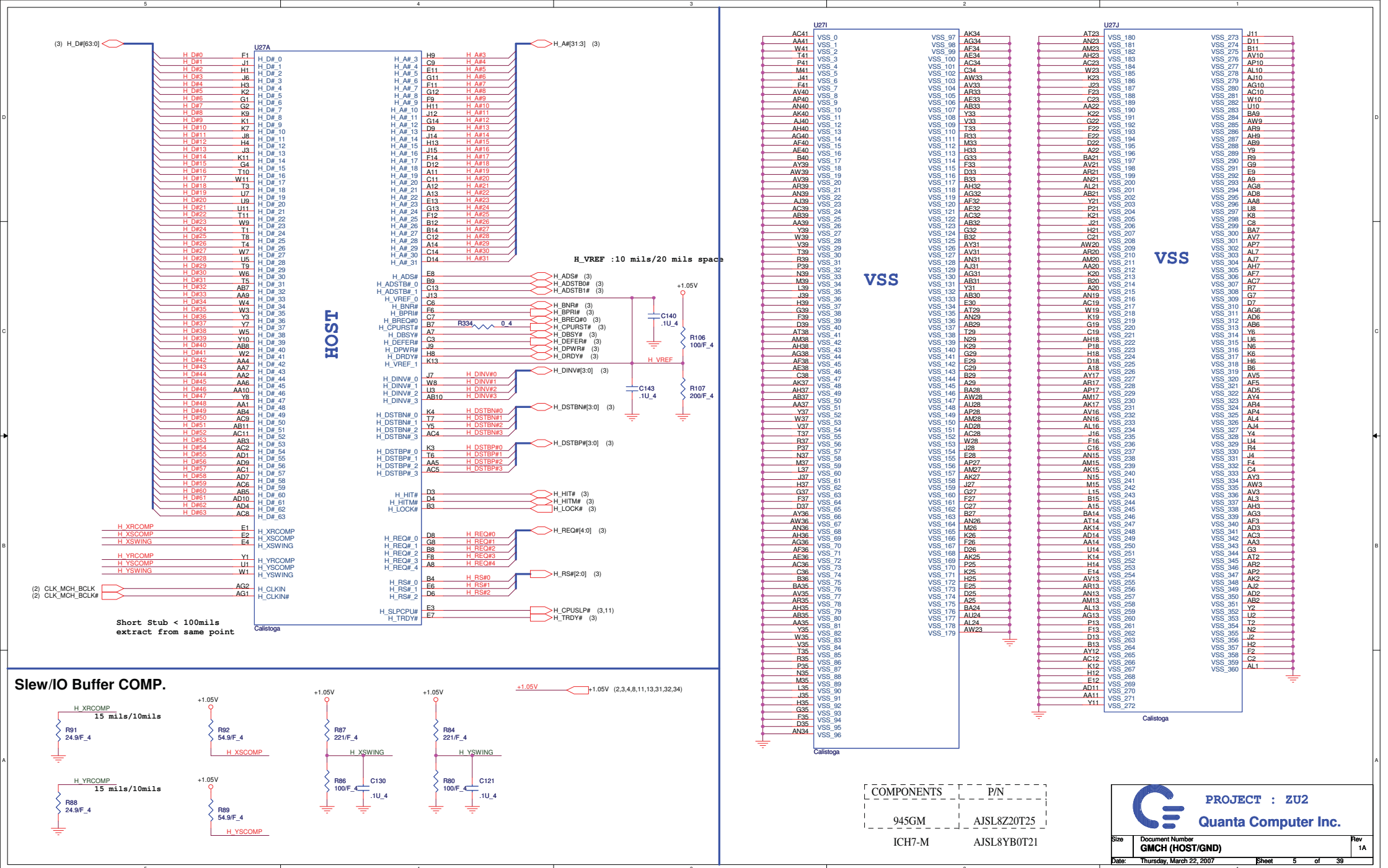
BSEL strappings need to be set for 533MHz Moby Dick
(Intel?915GM - Calistoga Interposer)
(if Calistoga is designed for 667MHz board).

```





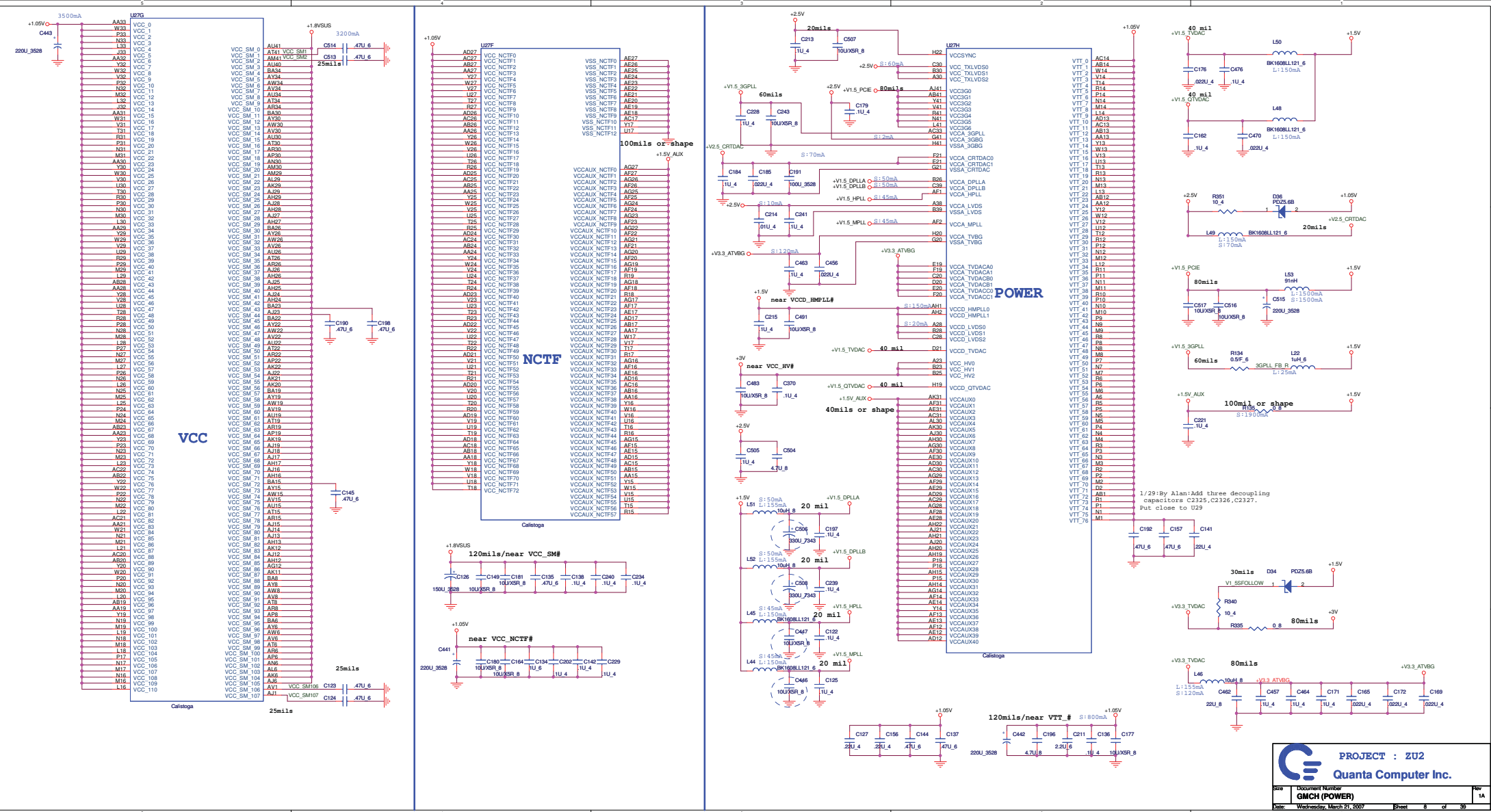




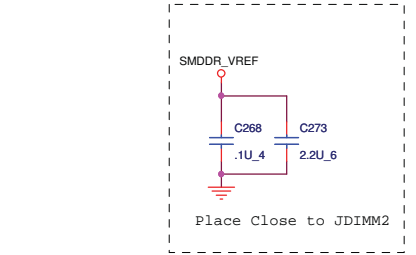
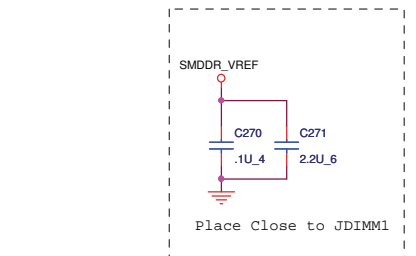
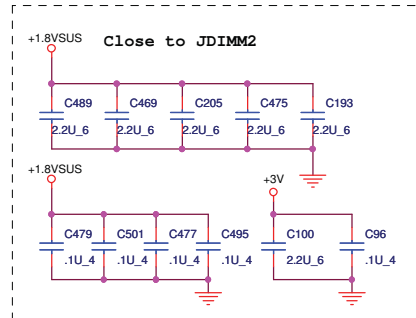
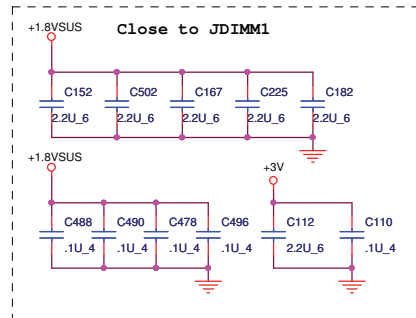
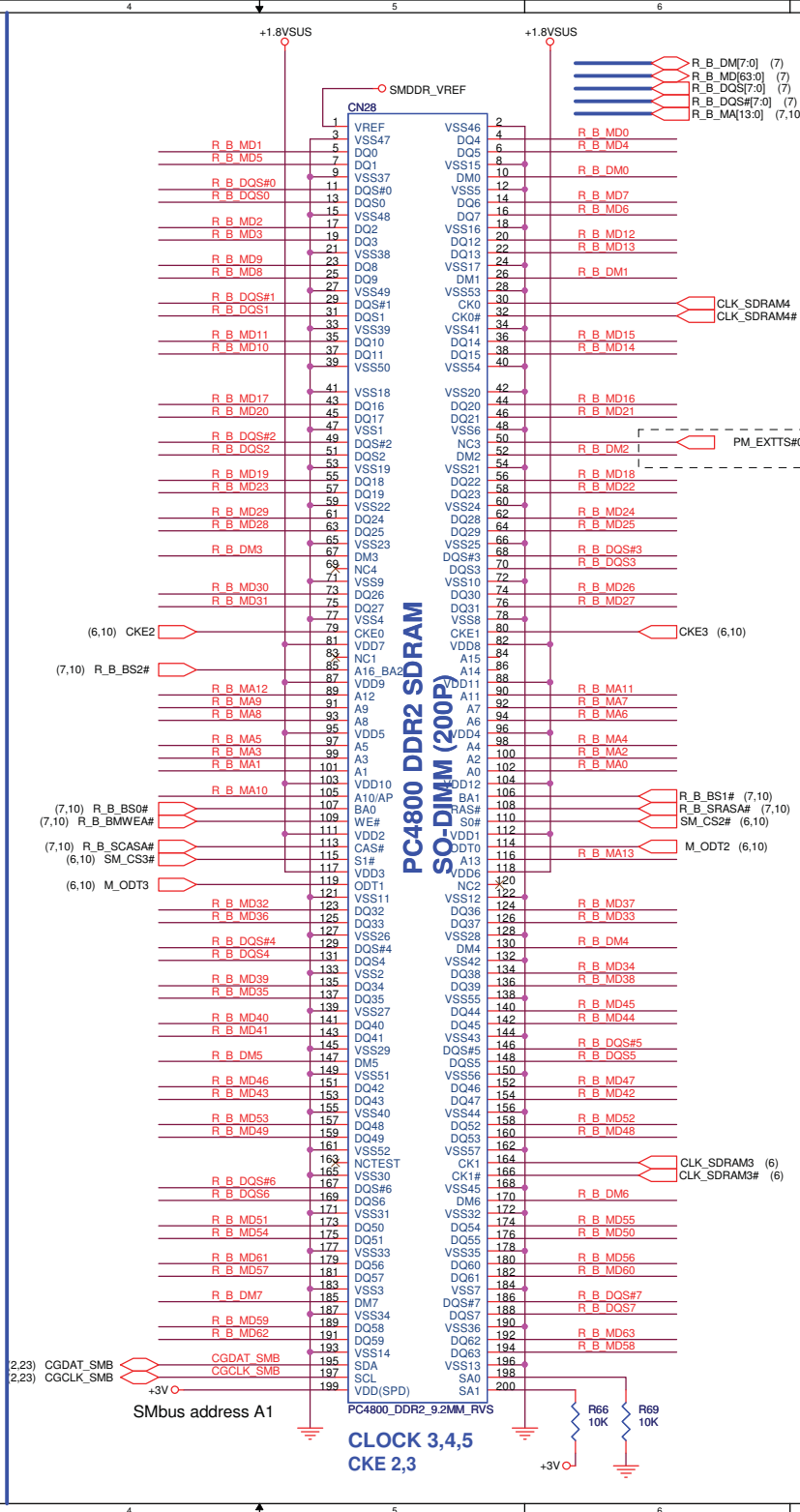
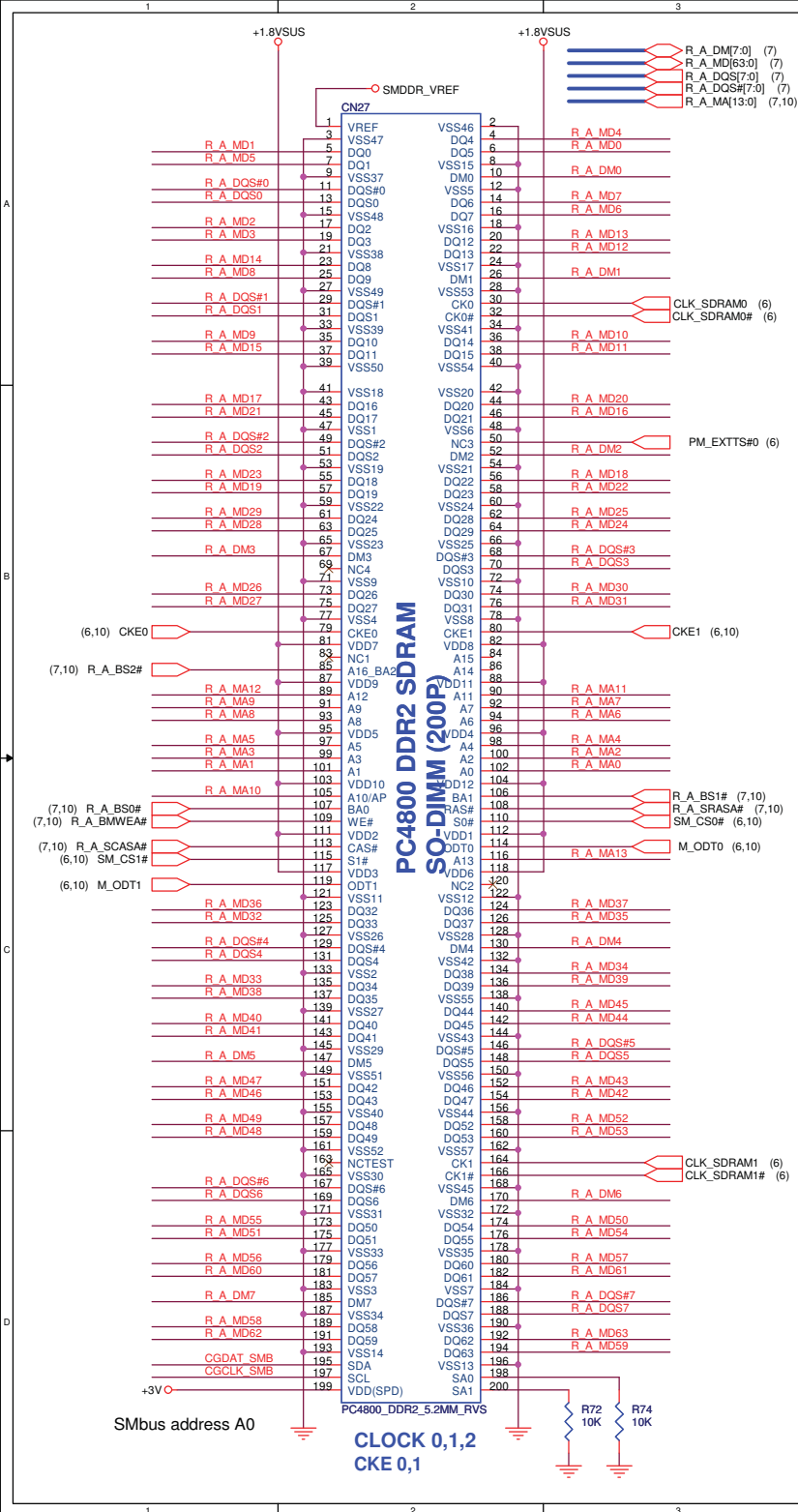






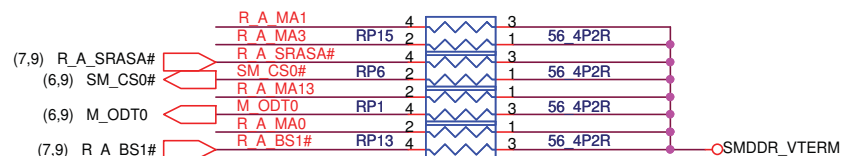
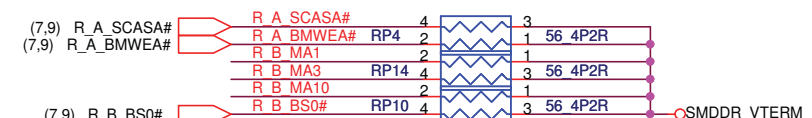
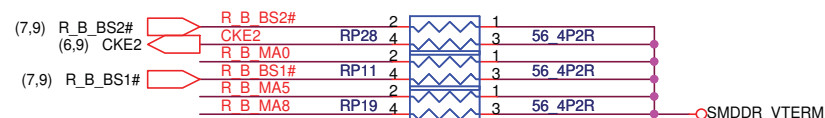
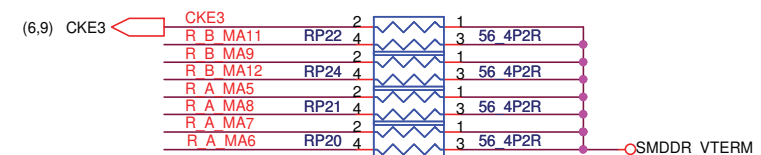
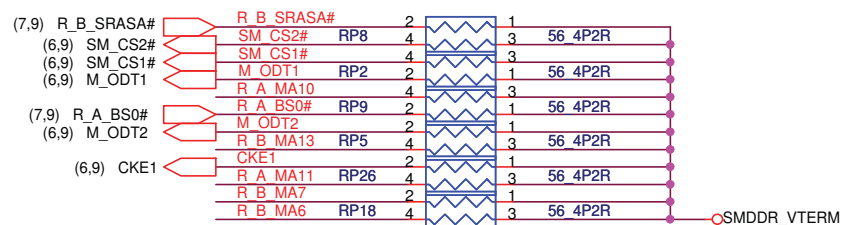
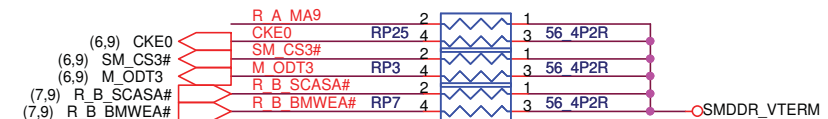
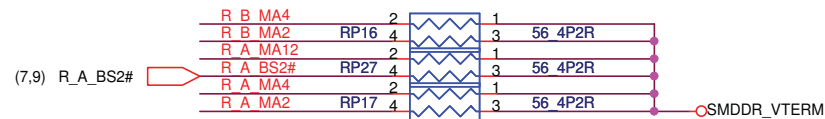






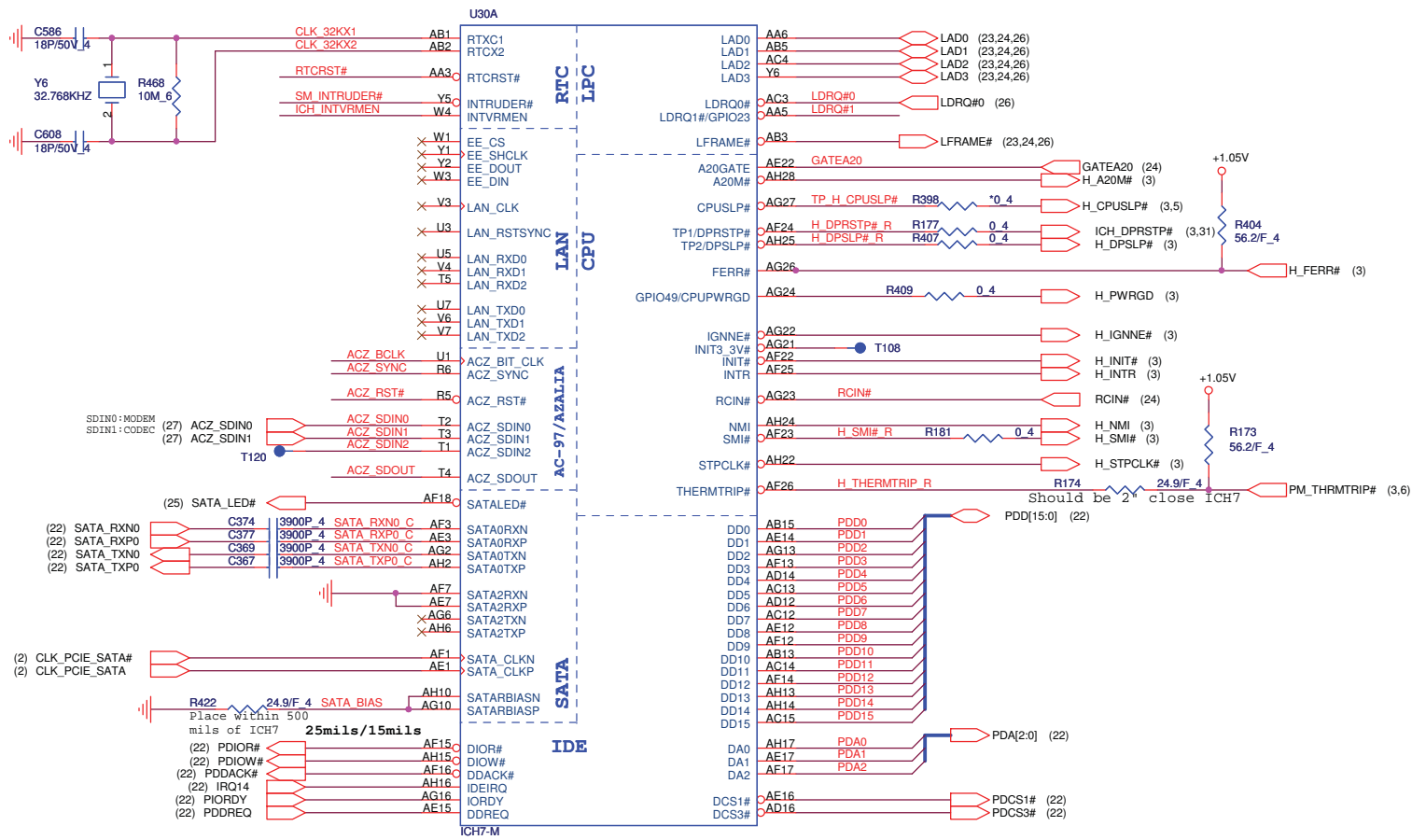
**Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR\_VTERM**

**Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR\_VTERM**

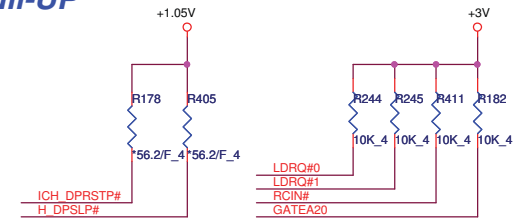


**PROJECT : ZU2**  
**Quanta Computer Inc.**

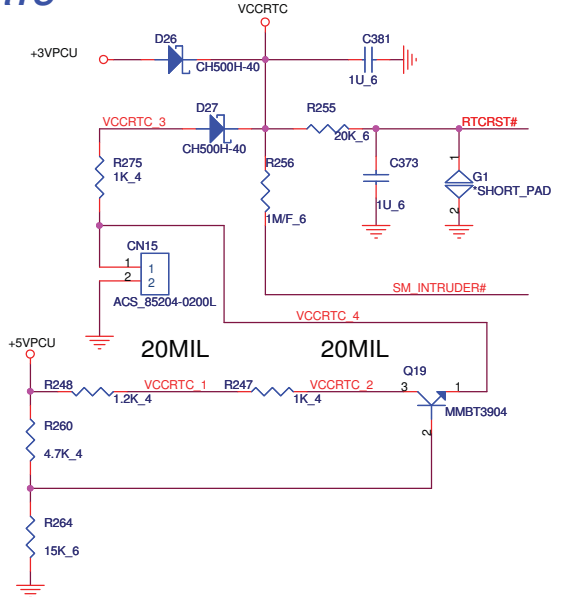
Size	Document Number <b>DDR2 RES. ARRAY</b>	Rev 1A
Date:	Friday, March 23, 2007	Sheet 10 of 39



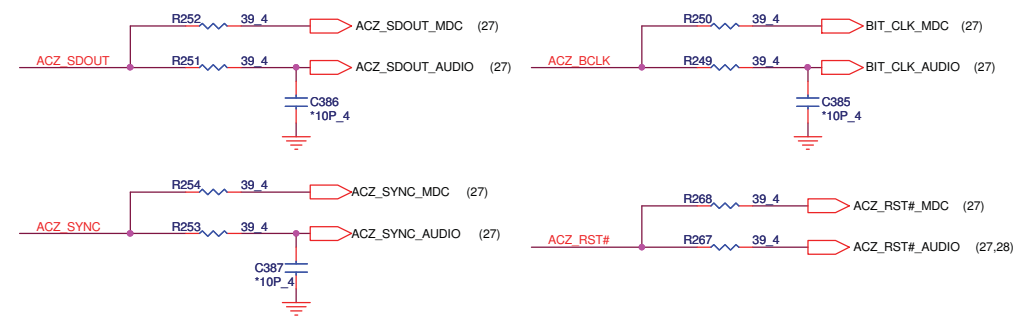
## Pull-UP



## RTC

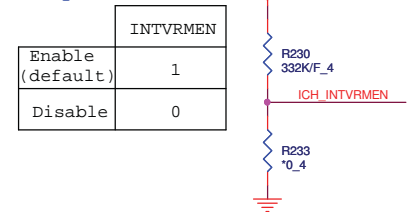


## HDA interface



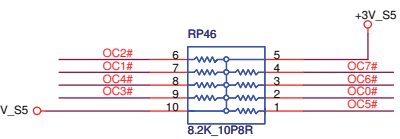
COMPONENTS	P/N
945GM	AJSL8Z20T25
ICH7-M	AJSL8Y0T21

## ICH7 internal VR enable strap

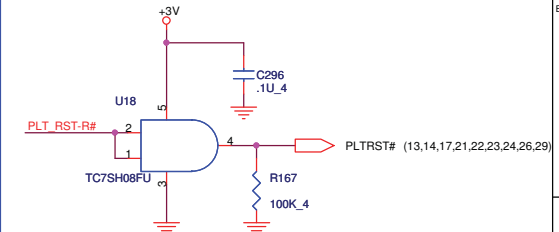


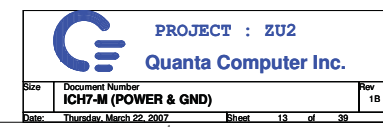
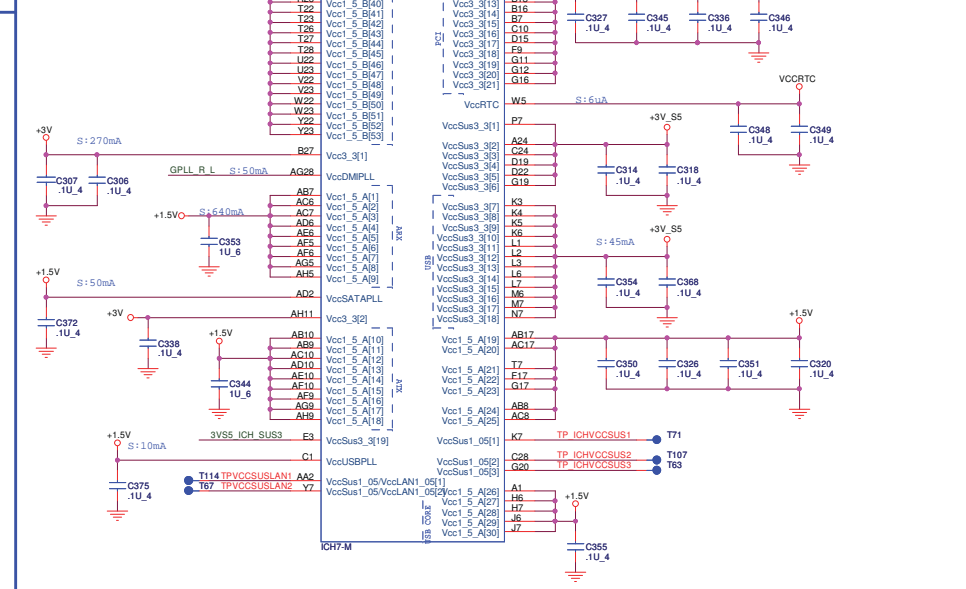
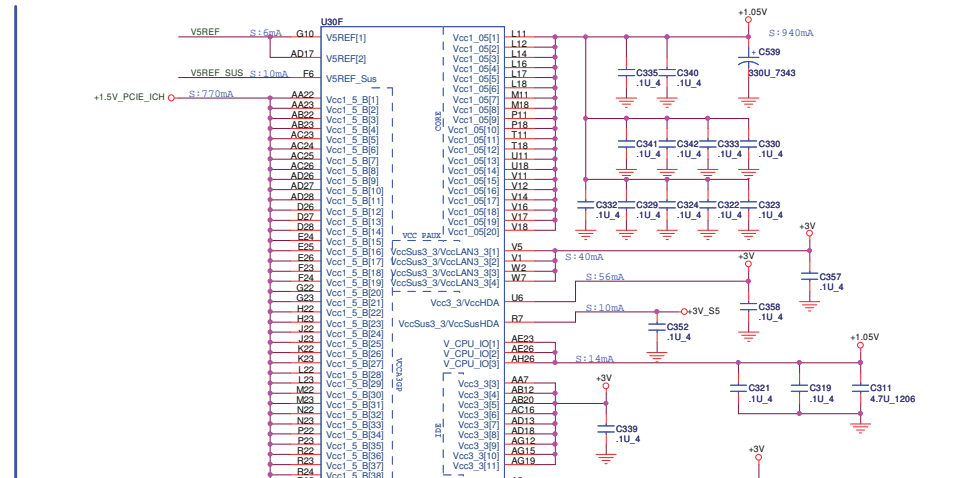
**PROJECT : ZU2**  
**Quanta Computer Inc.**

Size	Document Number	Rev
	ICH7-M (CPU, SATA, IDE, LPC)	1A



## Platform Reset

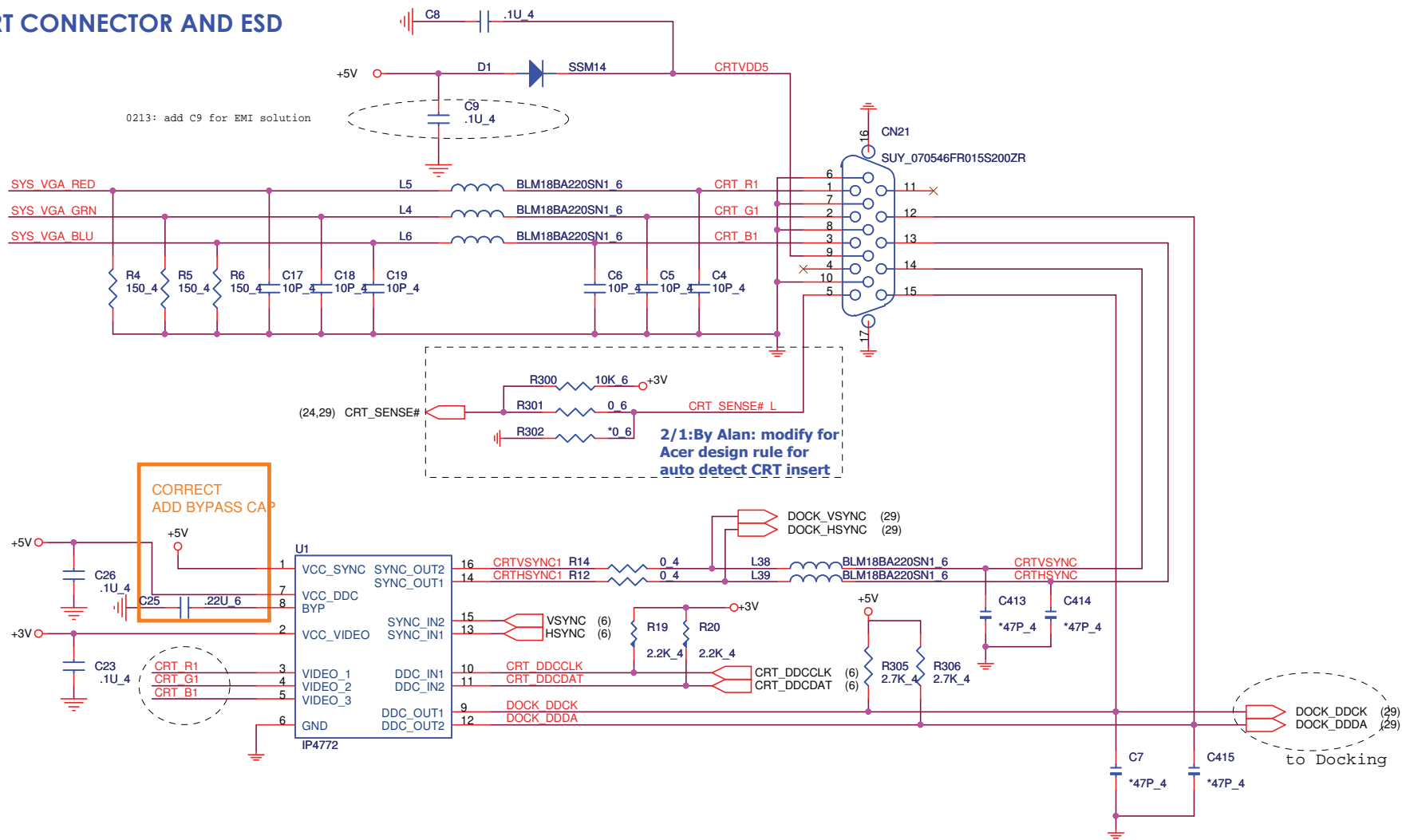




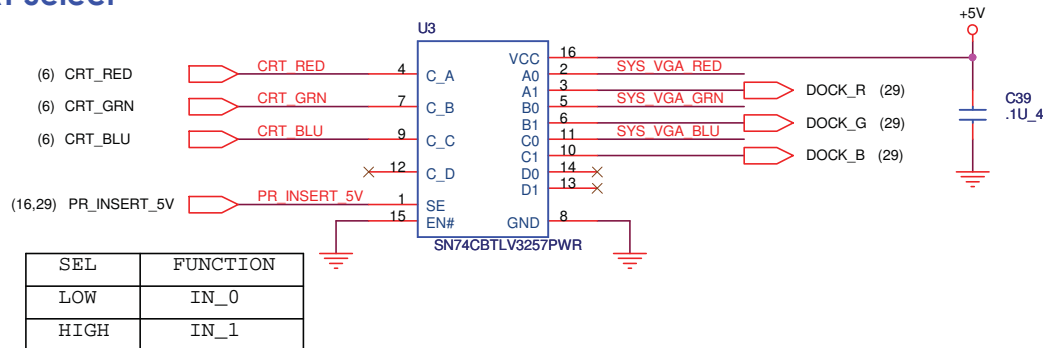




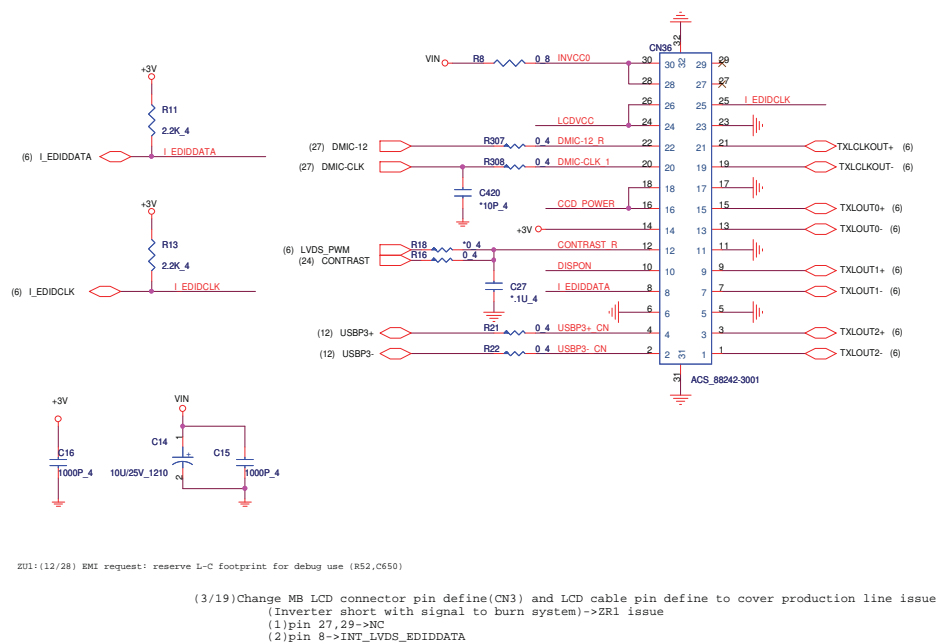
# CRT CONNECTOR AND ESD



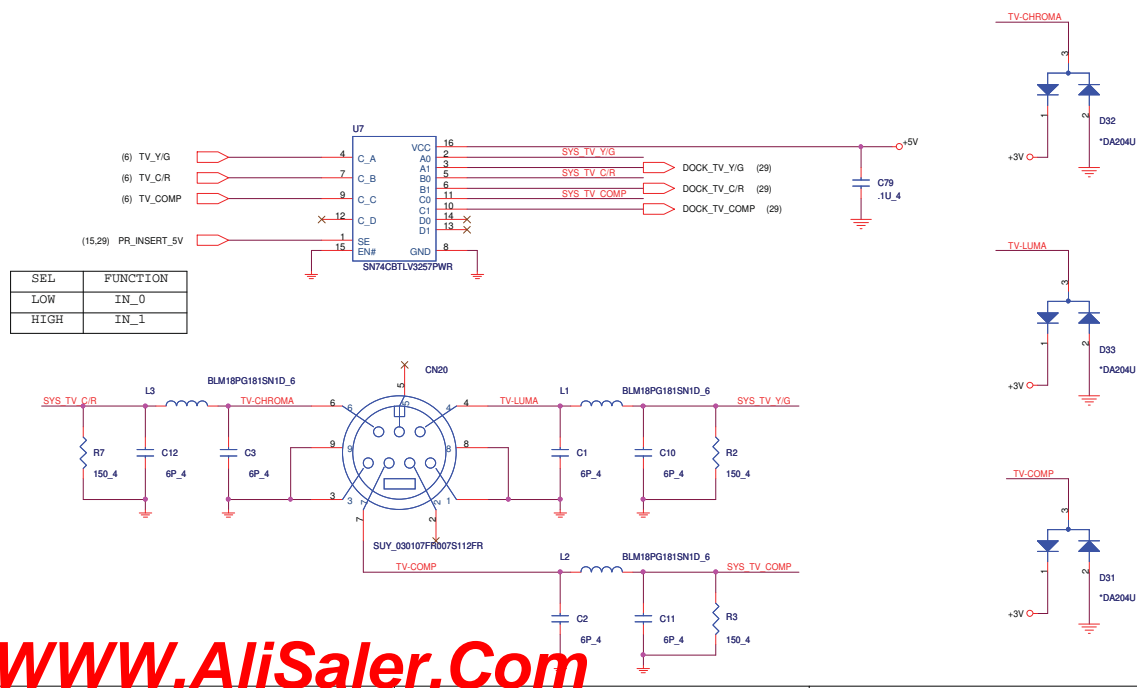
## CRT Select



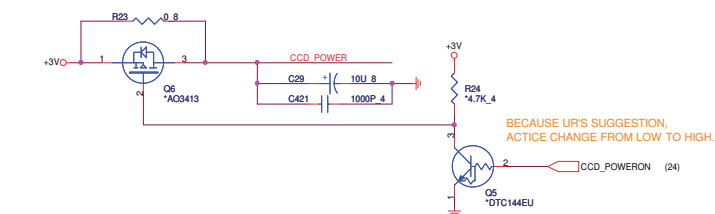
## LVDS



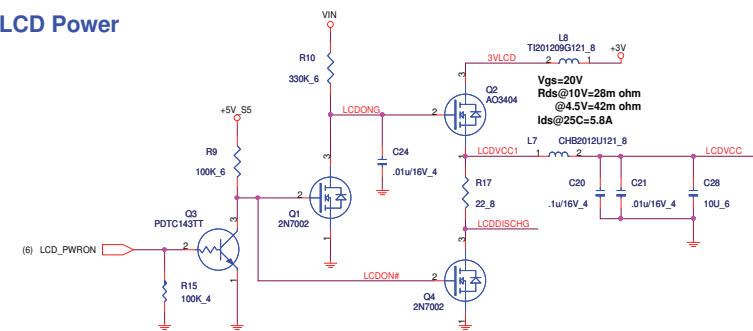
**TV Out (SVHS) MiniDIN 7-pin**



## CAMERA MODULE

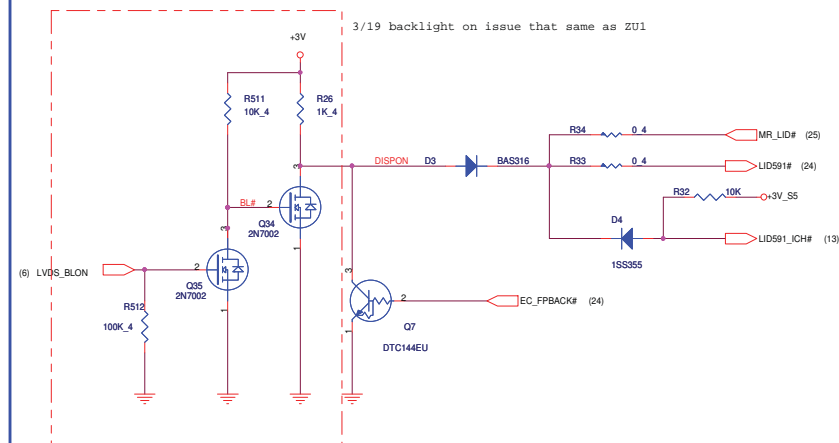


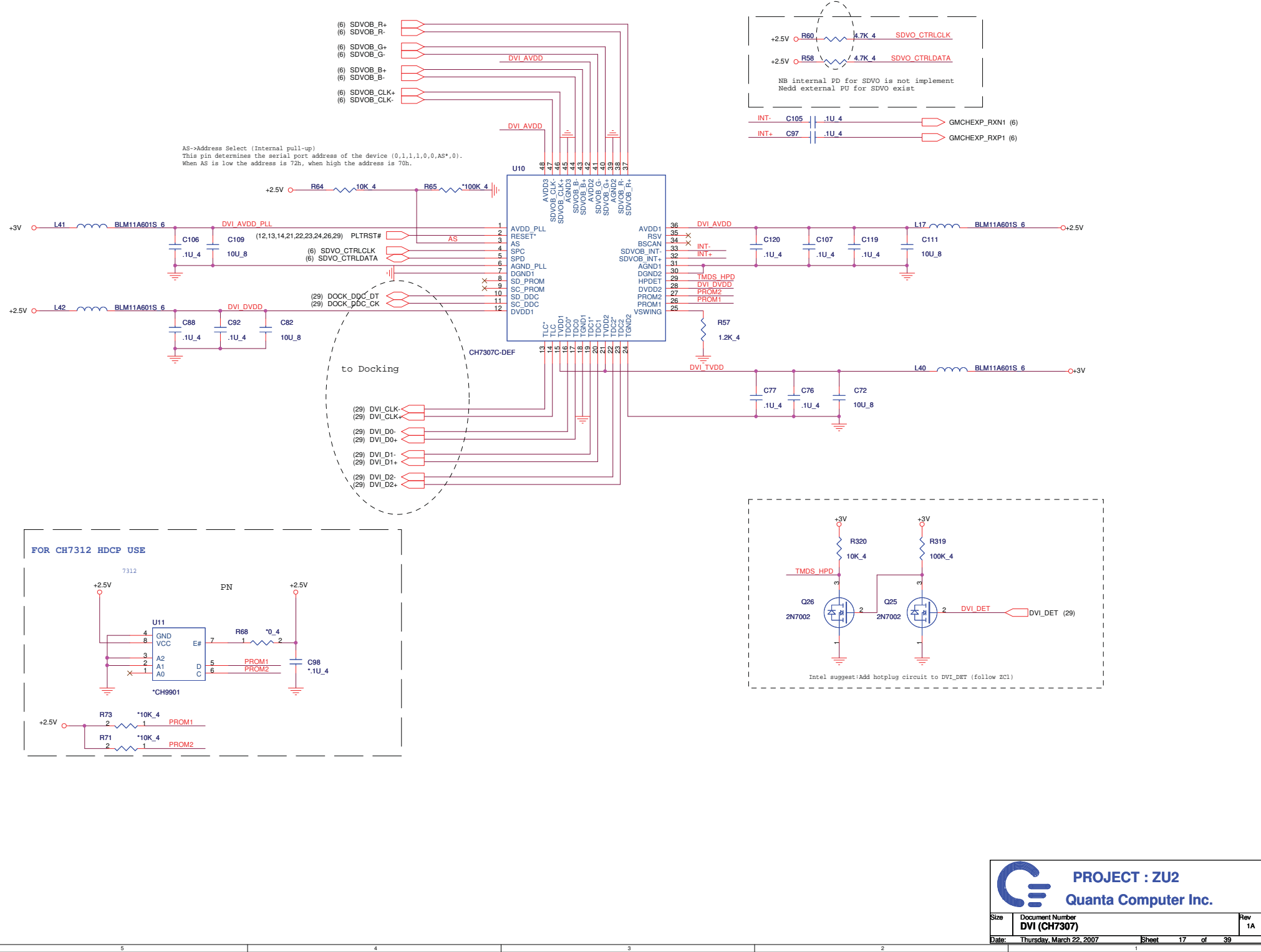
## LCD Power



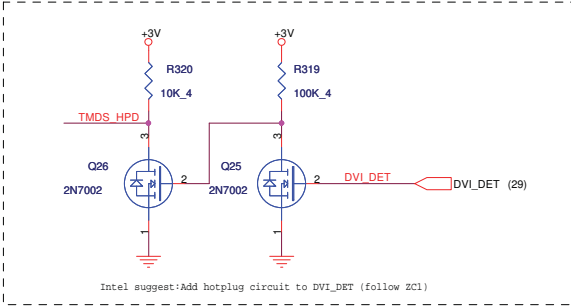
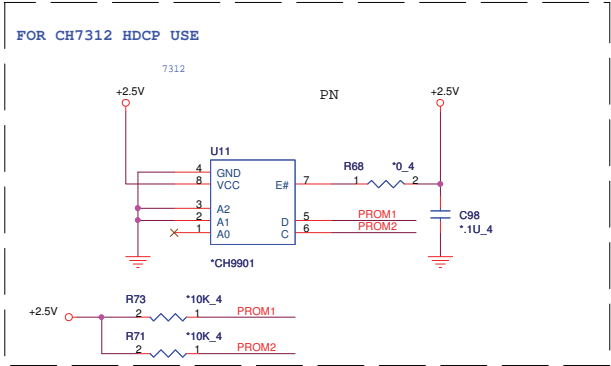
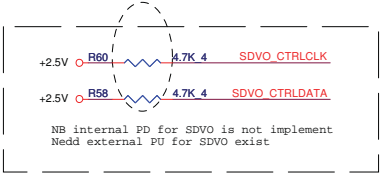
1/29:By Alan: AAT4280 fail on power ON rising time and falling time. EA.Additionally, some LCD panels will have garbage. follow ZR1 circuit.

## MR Sensor





AS->Address Select (Internal pull-up)  
This pin determines the serial port address of the device (0,1,1,1,0,0,AS\*,0).  
When AS is low the address is 72h, when high the address is 70h.



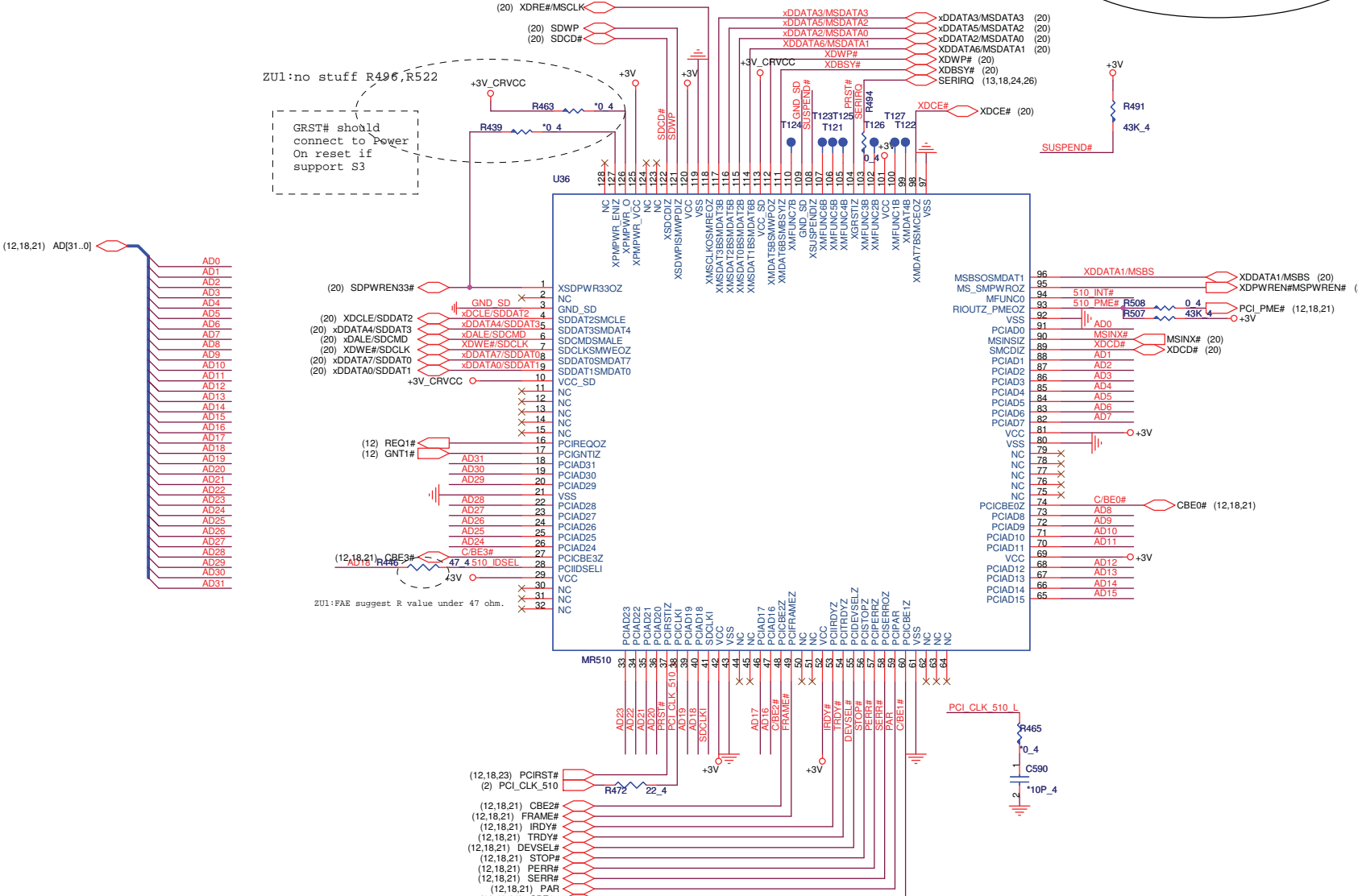




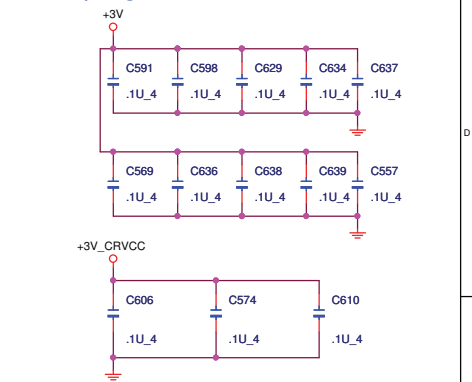
```

ID Select      : AD18
Interrupt Pin   : INTG#
Request Indicate : REQ1#
Grant Indicate  : GNT1#

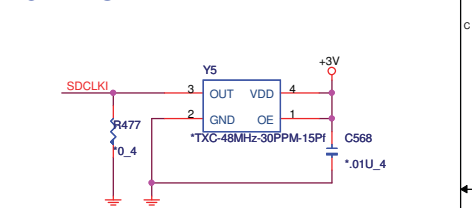
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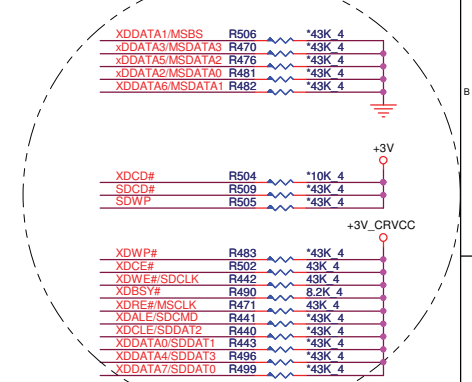
## Decoupling CAP.



## 48MHz CLK



PU/PD



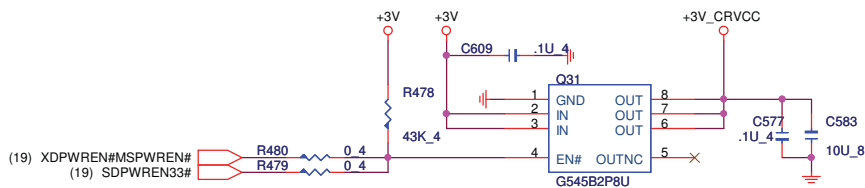
0213: follow ZU1 for cost down issue with FAE



 PROJECT : ZU2  
Quanta Computer Inc.

Size	Document Number	Rev
	<b>Card Reader (MR510)</b>	18
Date:	Wednesday, March 28, 2007	Sheet 19 of 39

## CardReader Power switch



AVCC

R377 43K 4 A CAD11

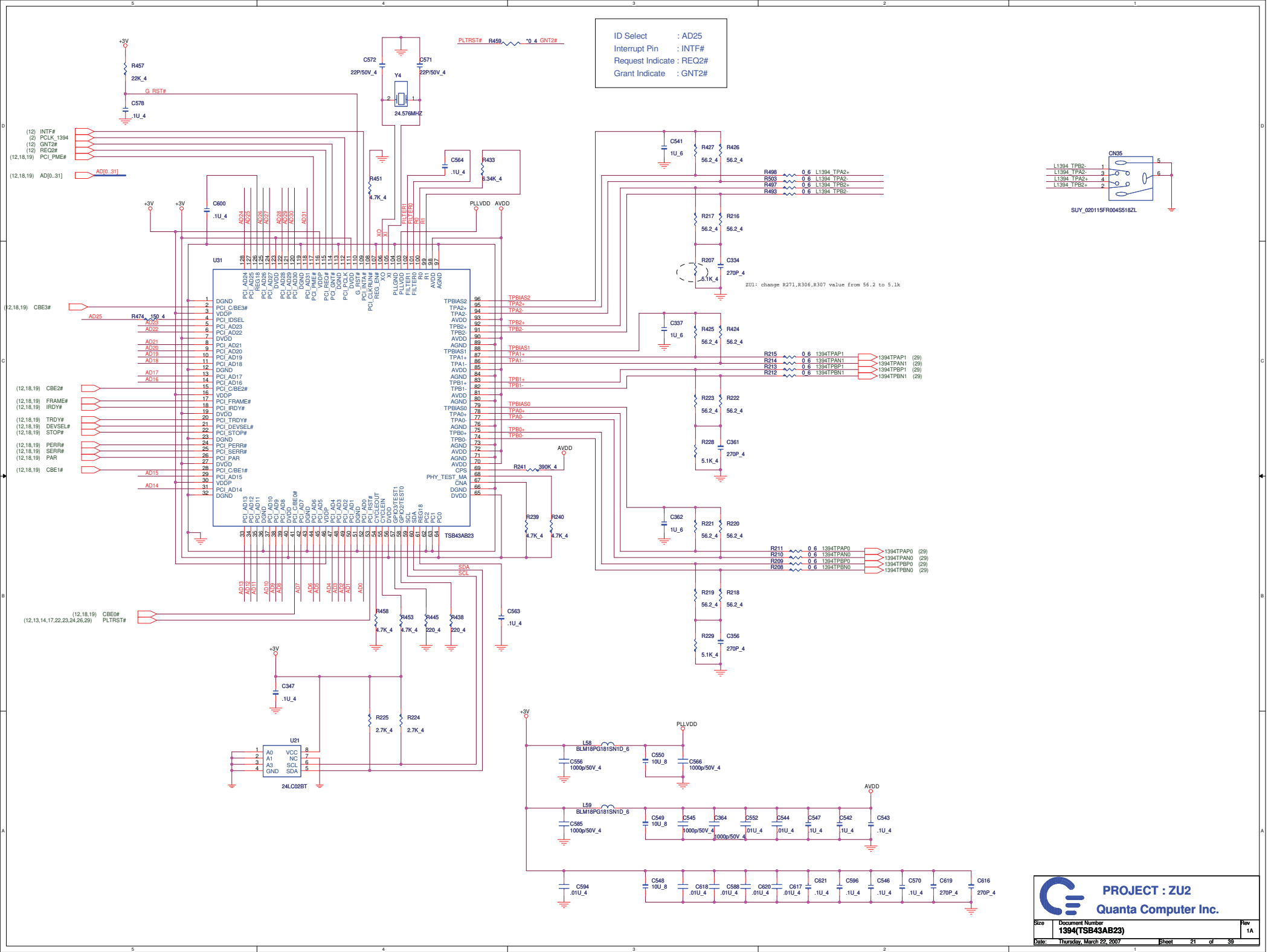
GN16 FOX WZ21131-G2-8F

(18) A\_CAD0# A\_CAD0# 2  
(18) A\_CAD1# A\_CAD1# 3  
(18) A\_CAD3# A\_CAD3# 4  
(18) A\_CAD5# A\_CAD5# 5  
(18) A\_CAD7# A\_CAD7# 6  
(18) A\_CC/BE0# A\_CC/BE0# 7  
(18) A\_CAD9# A\_CAD9# 8  
(18) A\_CAD11# A\_CAD11# 9  
(18) A\_CAD12# A\_CAD12# 10  
(18) A\_CAD14# A\_CAD14# 11  
(18) A\_CC/BE1# A\_CC/BE1# 12  
(18) A\_CPAR# A\_CPAR# 13  
(18) A\_CPERR# A\_CPERR# 14  
(18) A\_CGNT# A\_CGNT# 15  
(18) A\_CINT# A\_CINT# 16  
AVCC 17  
VPP1 18  
(18) A\_CCLK# A\_CCLK# 19  
(18) A\_CIRDY# A\_CIRDY# 20  
(18) A\_CC/BE2# A\_CC/BE2# 21  
(18) A\_CAD18# A\_CAD18# 22  
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(18) A\_CAD23# A\_CAD23# 26  
(18) A\_CAD24# A\_CAD24# 27  
(18) A\_CAD25# A\_CAD25# 28  
(18) A\_CAD26# A\_CAD26# 29  
(18) A\_CAD27# A\_CAD27# 30  
(18) A\_CAD29# A\_CAD29# 31  
(18) A\_CRSVD/D2 A\_CRSVD/D2 32  
(18) A\_CCLKRUN# A\_CCLKRUN# 33  
AVCC 34  
(18) A\_CCD1# A\_CCD1# 35  
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(18) A\_CAD4# A\_CAD4# 37  
(18) A\_CAD6# A\_CAD6# 38  
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(18) A\_CAD15# A\_CAD15# 44  
(18) A\_CAD16# A\_CAD16# 45  
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(18) A\_CSTOP# A\_CSTOP# 48  
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AVCC 50  
VPP2 51  
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(18) A\_CFRAME# A\_CFRAME# 53  
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(18) A\_CC/BE3# A\_CC/BE3# 60  
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HOLE1 68  
HOLE2 69  
HOLE3 70  
HOLE4 71  
GND 72  
GND 73  
NPTH\_Hole 74  
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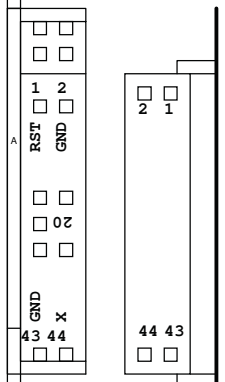
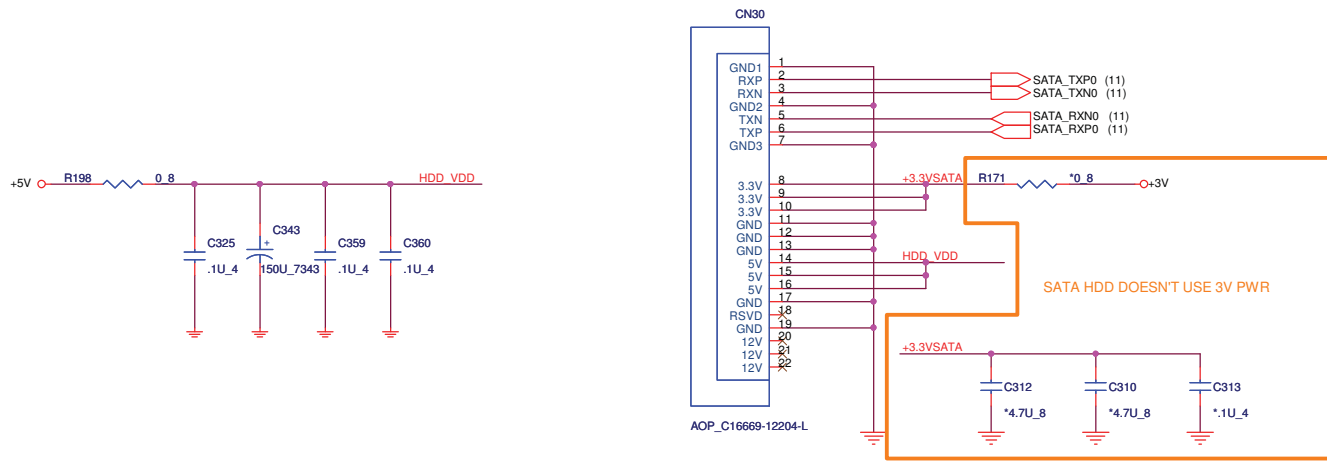


**PROJECT : ZU2**  
**Quanta Computer Inc.**

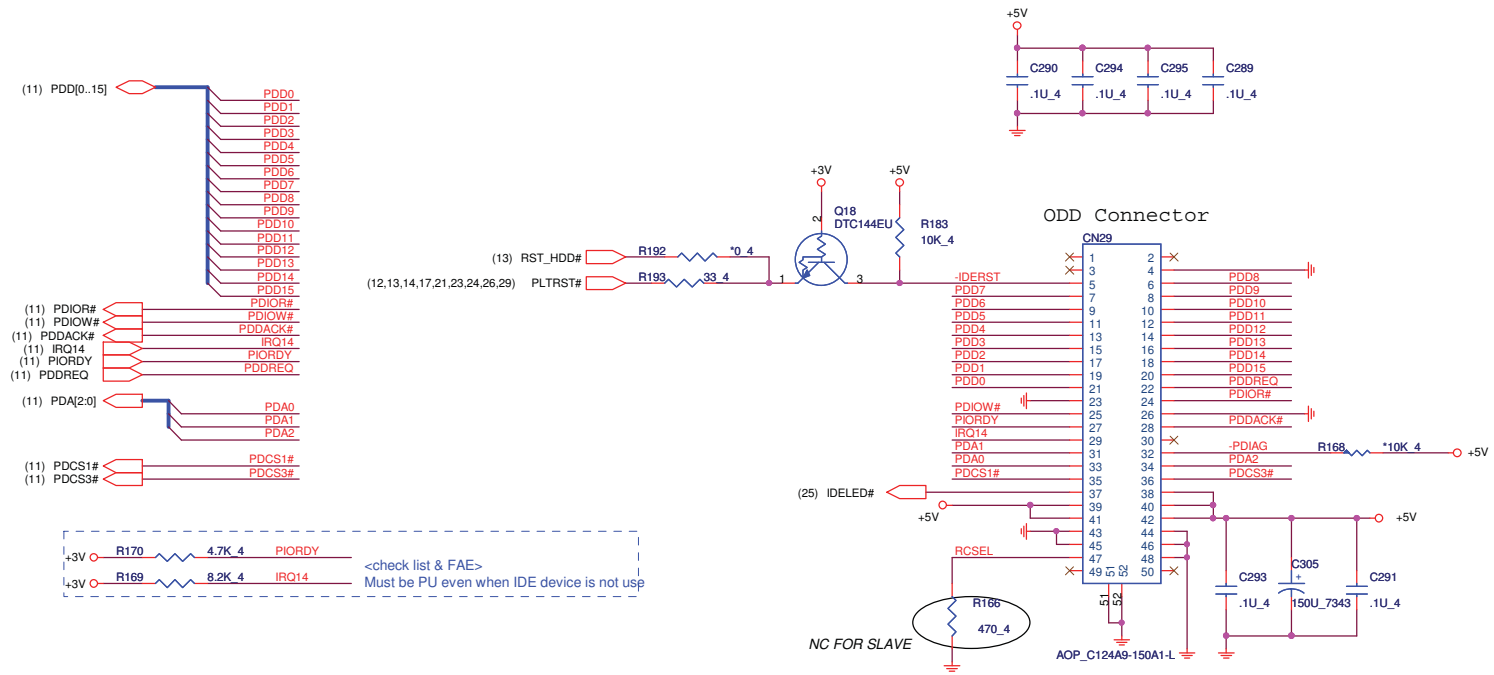
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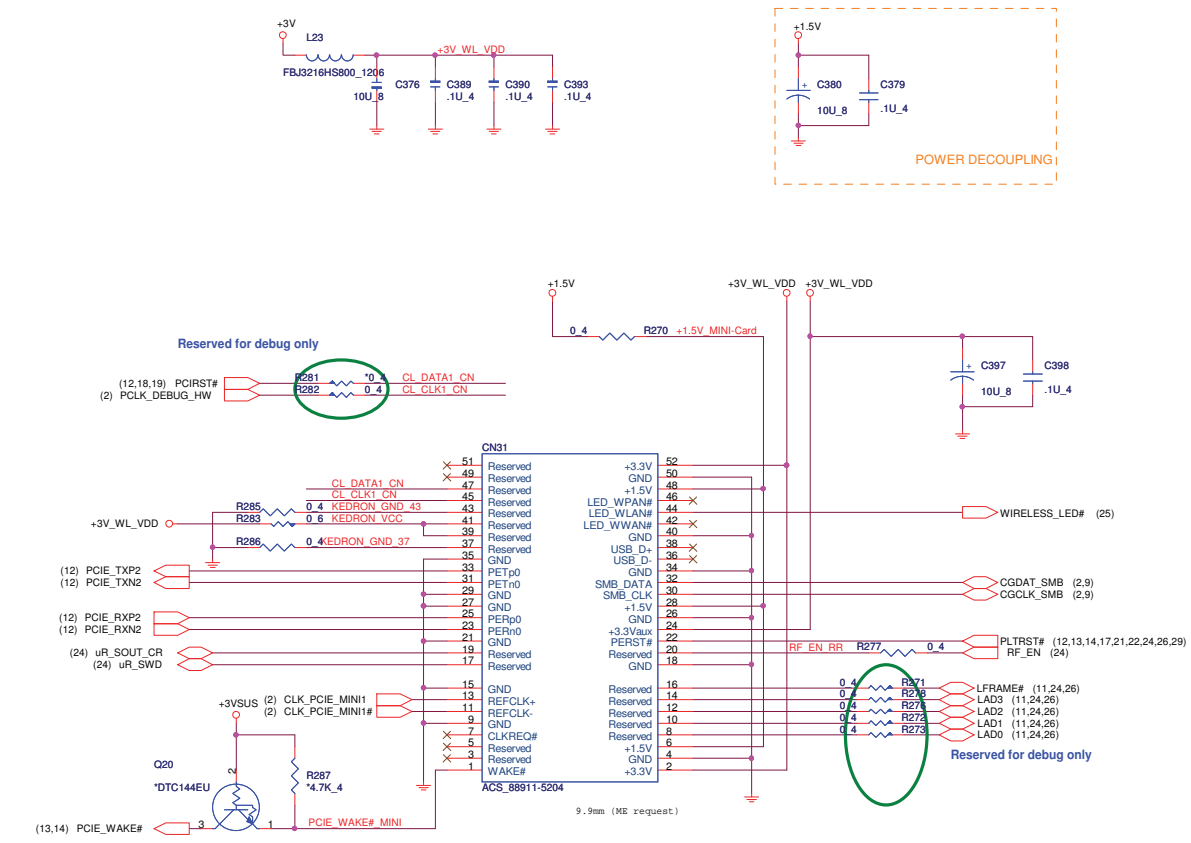
SATA HDD



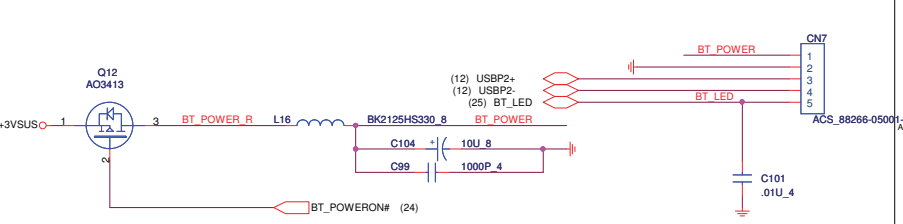
PATA ODD



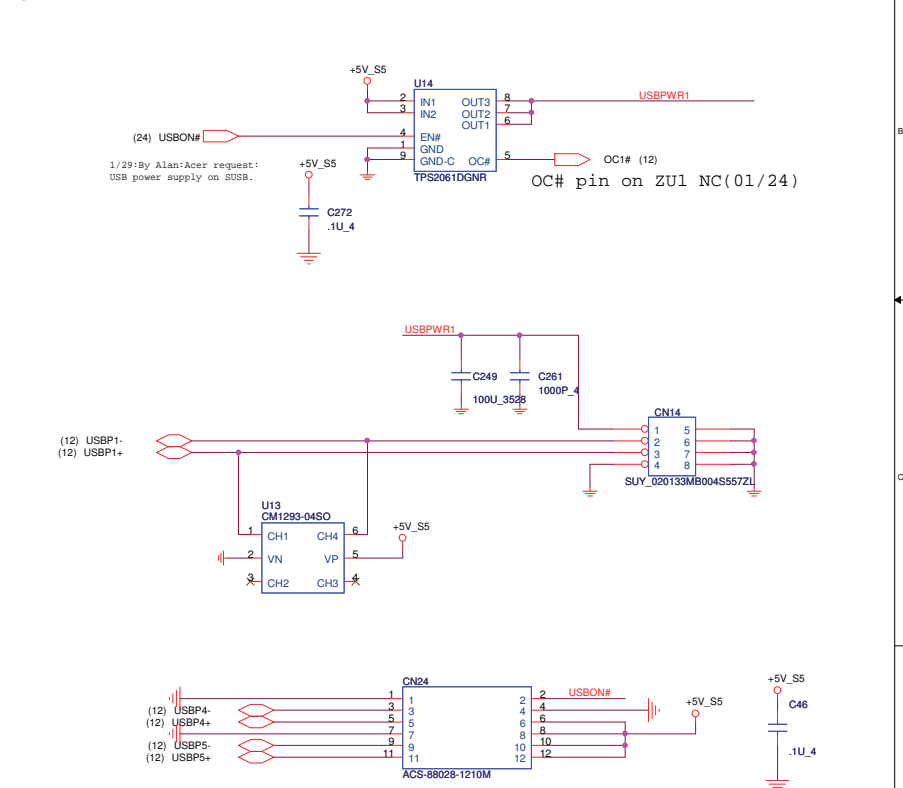
MINI-Card



BLUETOOTH MODULE CONNECTOR



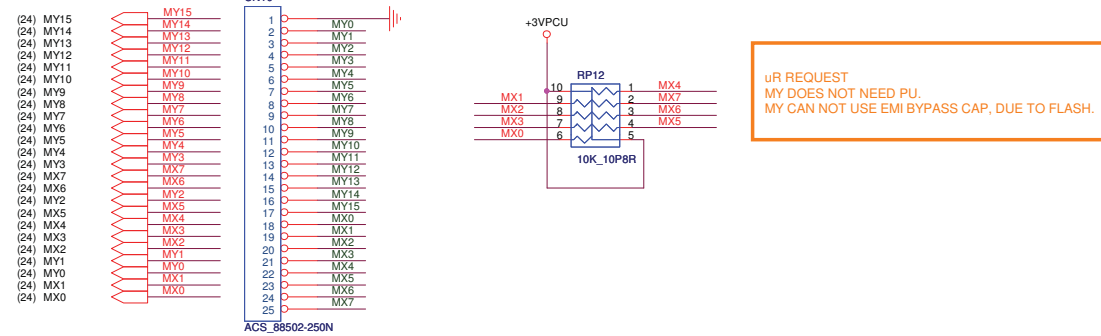
System USB



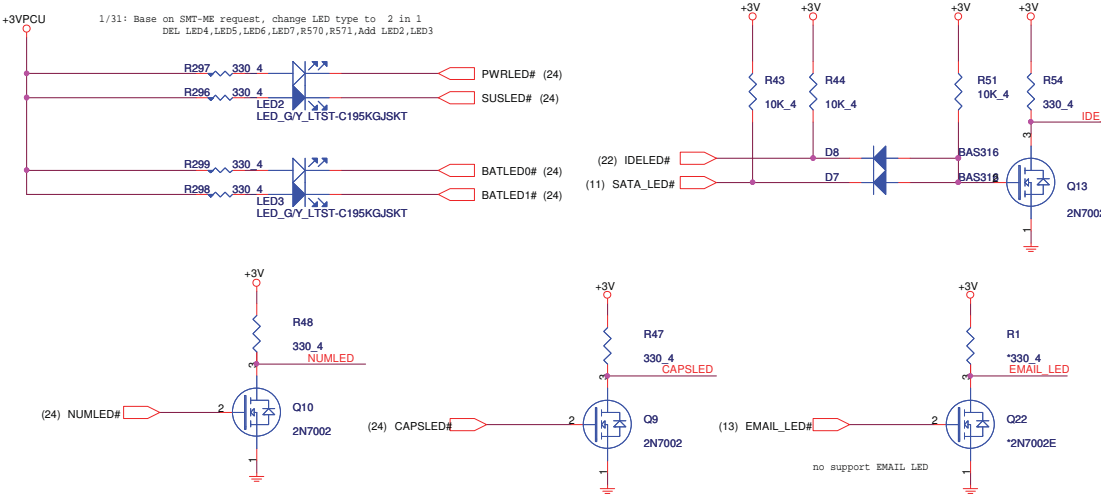




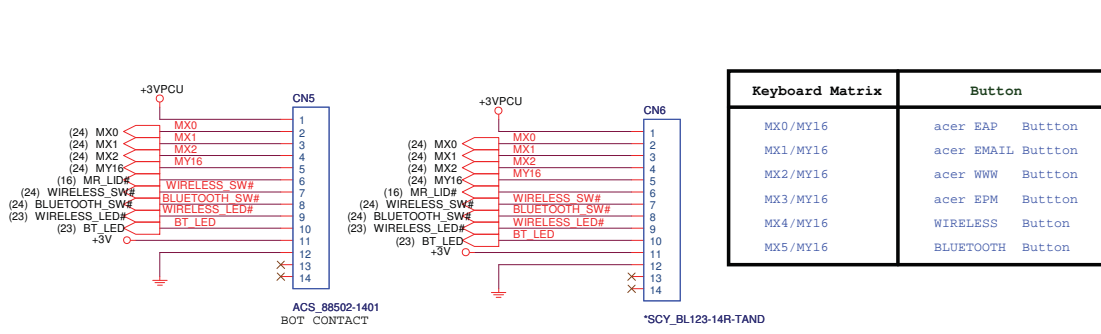
INT K/B



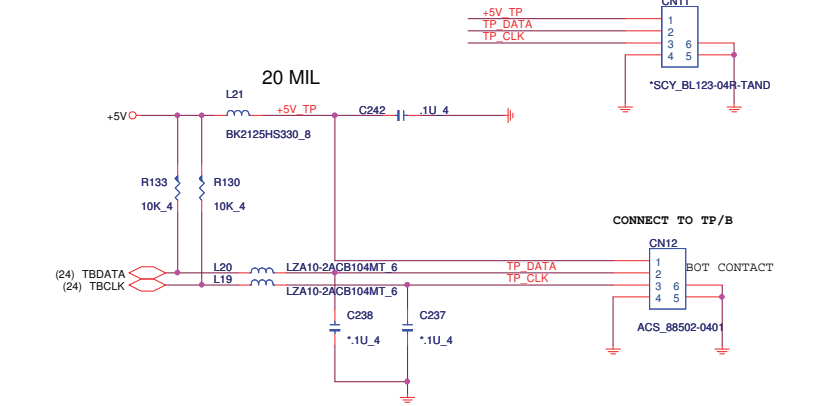
LED



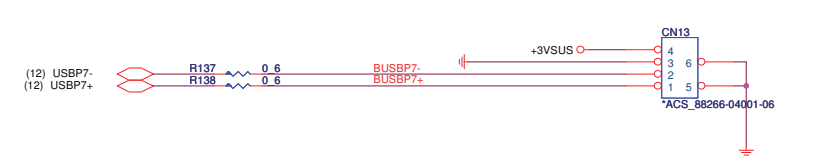
Function Board



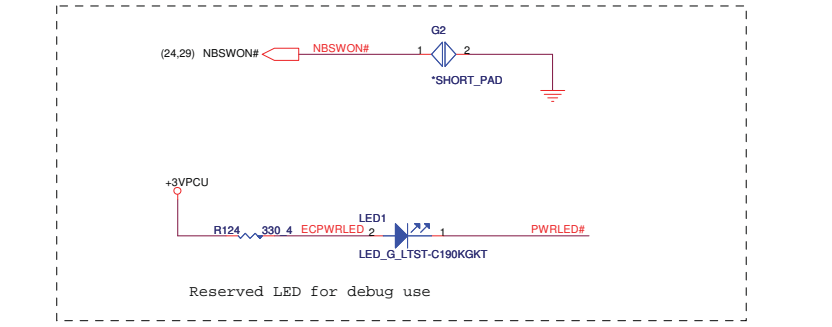
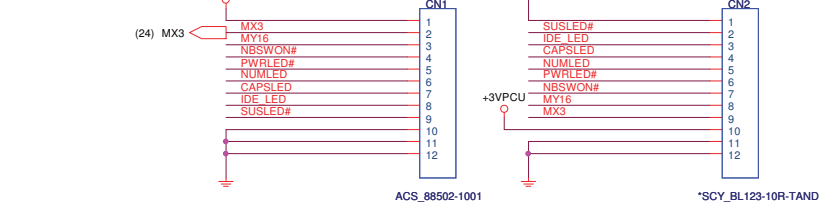
TOUCH PAD

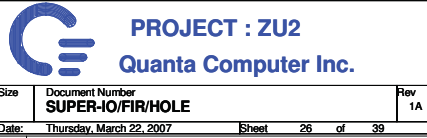


Finger Printer

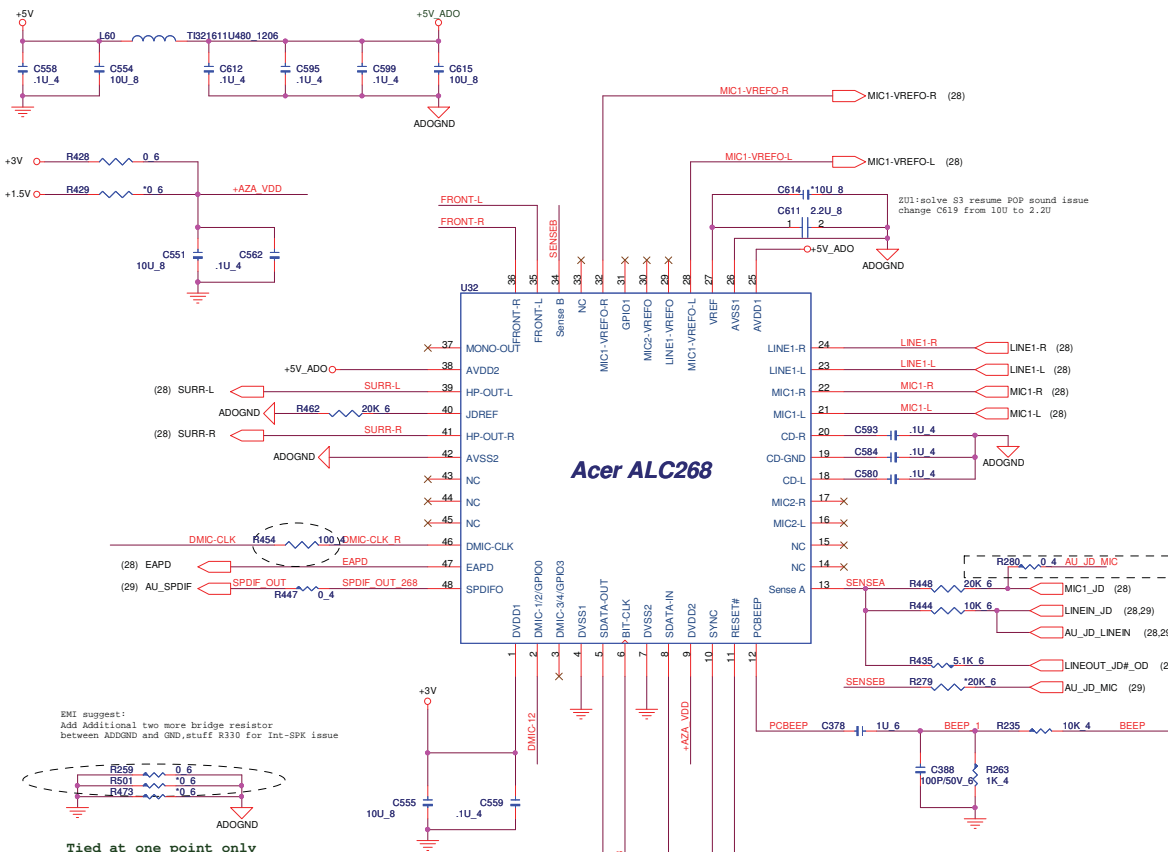


LED Board

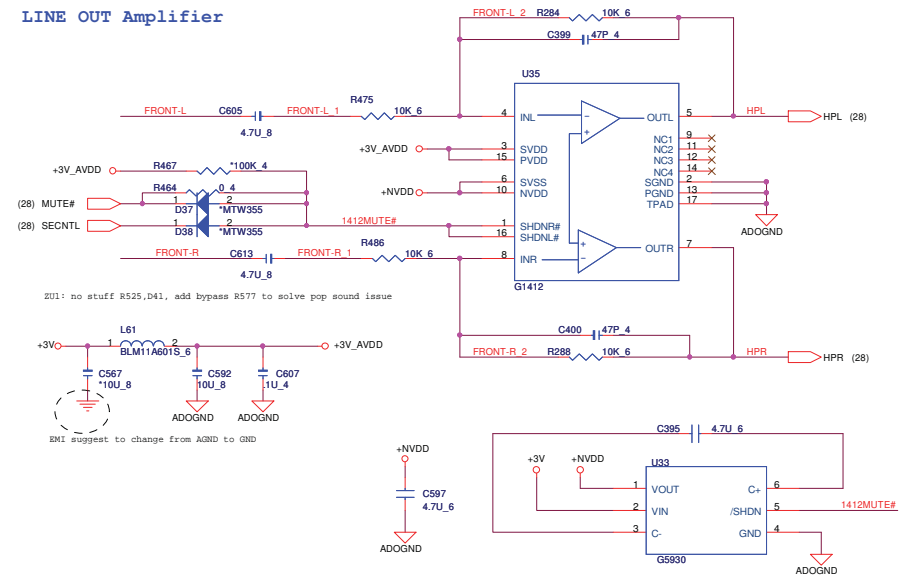




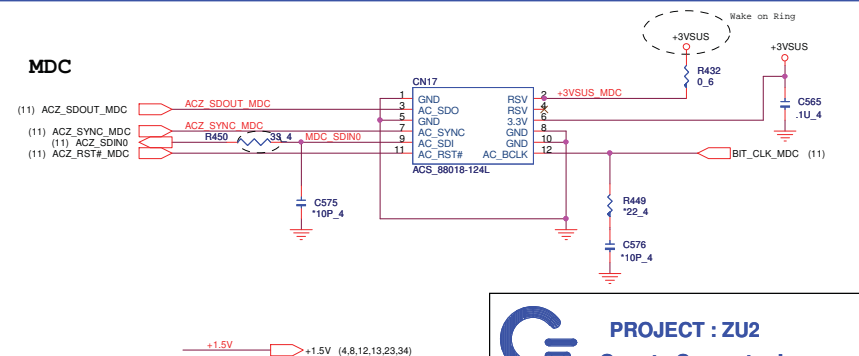
## CODEC (ALC268)



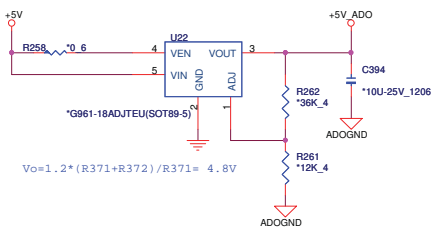
## LINE OUT Amplifier



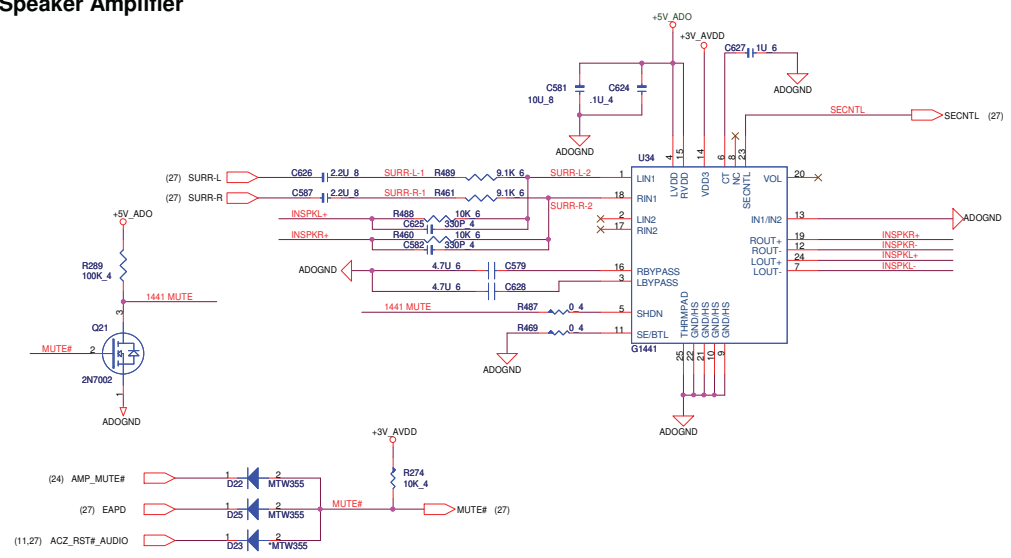
## MDC



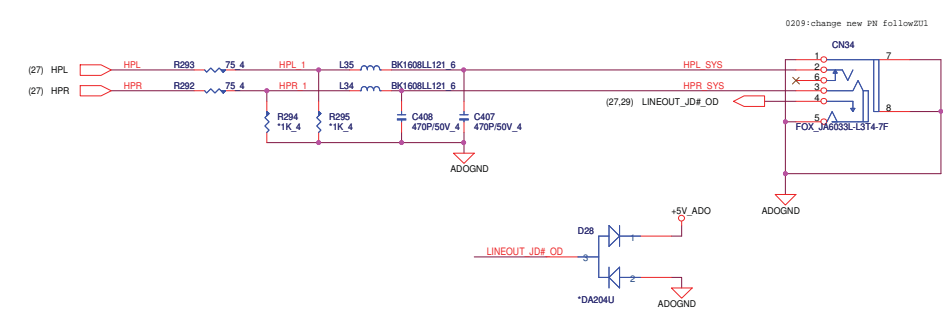
## Reserve Audio power



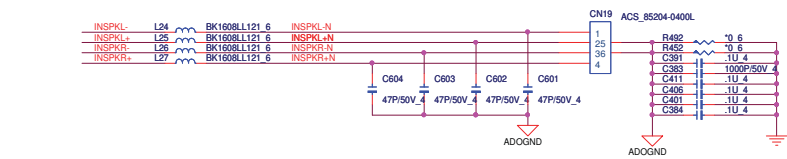
Speaker Amplifier



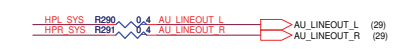
SYSTEM LINE OUT



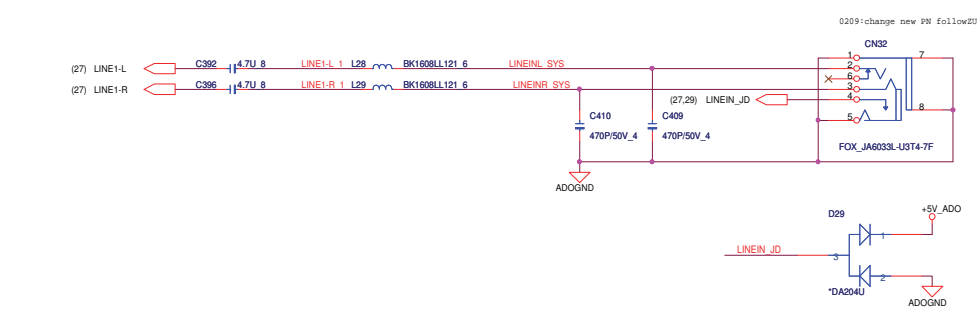
SPEAKER



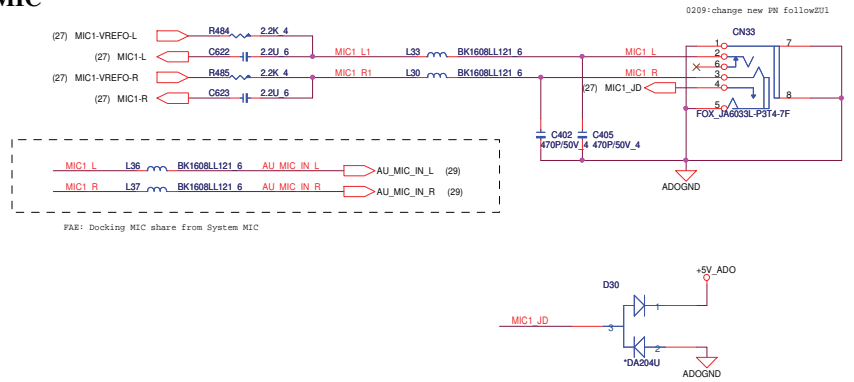
Docking LINE OUT/SPDIF



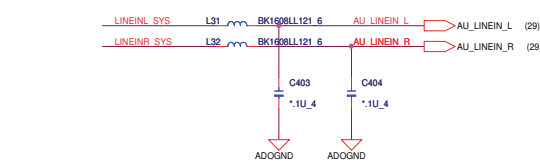
SYSTEM LINE IN



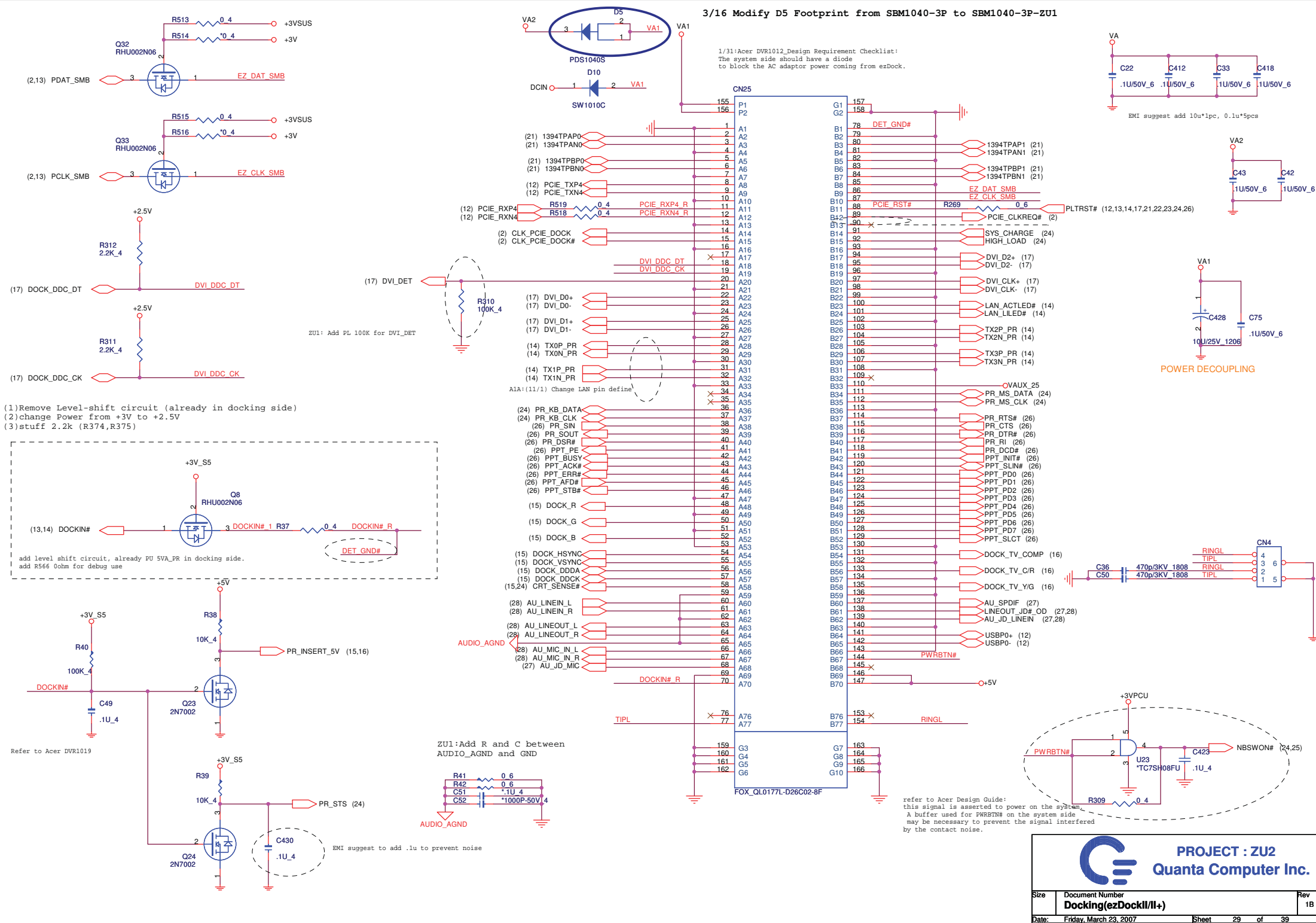
SYSTEM MIC



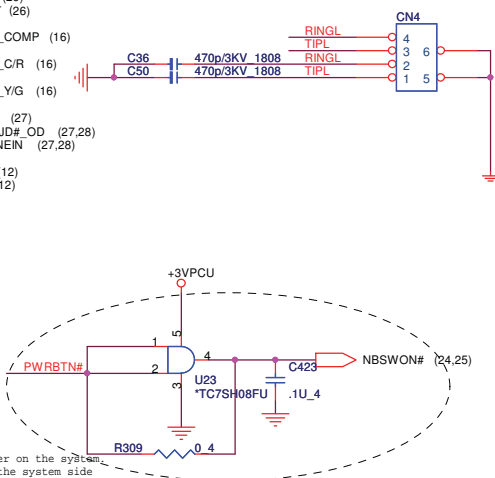
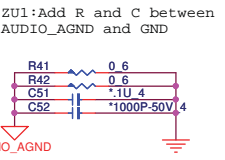
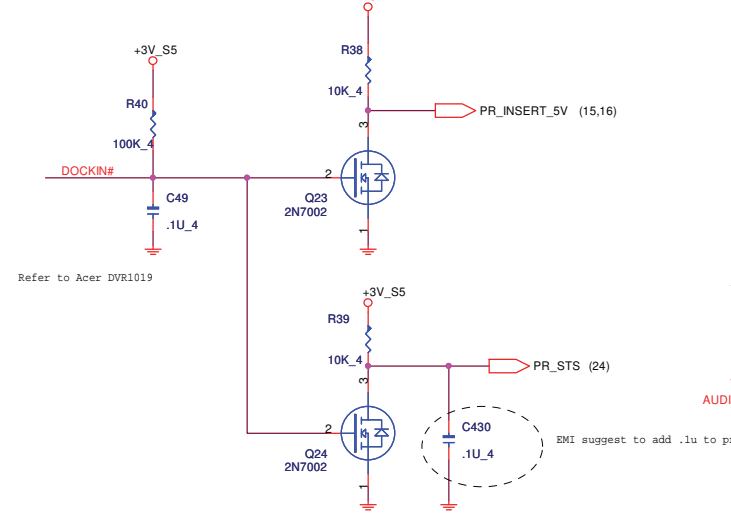
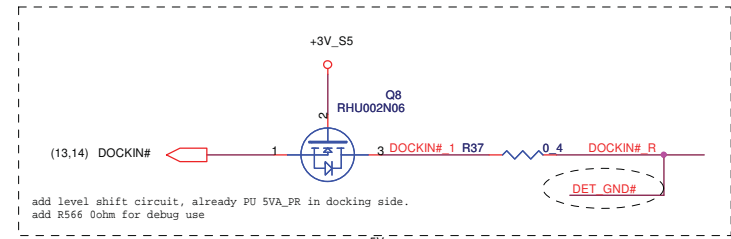
Docking LINE IN



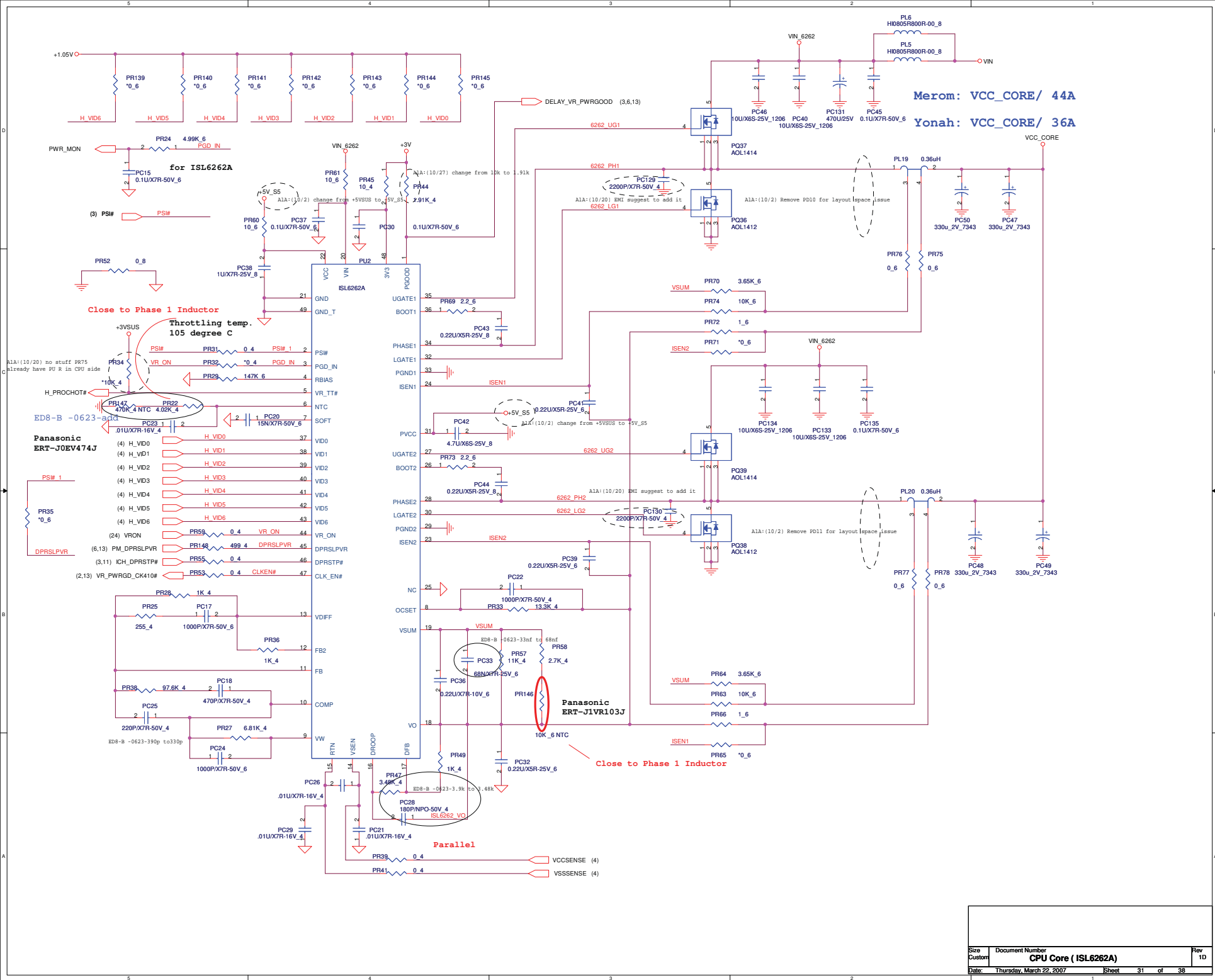




- (1) Remove Level-shift circuit (already in docking side)
- (2) change Power from +3V to +2.5V
- (3) stuff 2.2k (R374, R375)





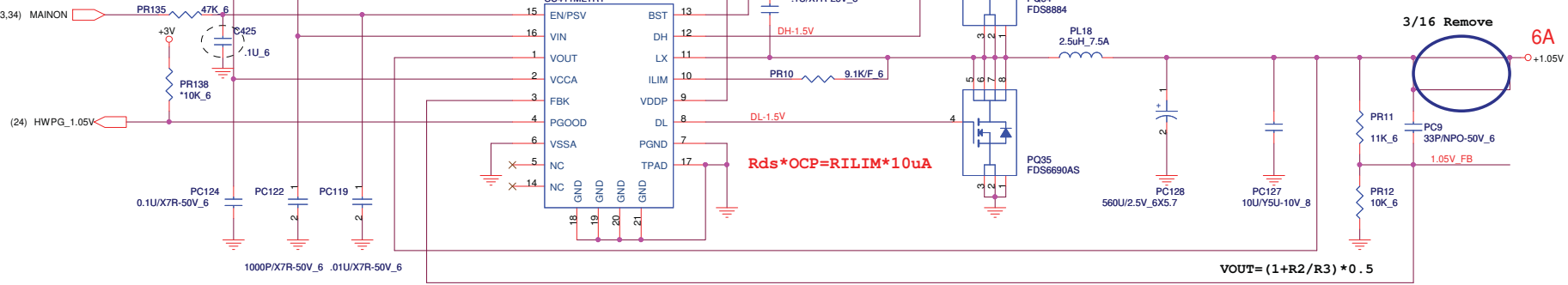


B1C:(11/30) T211 Power sequence issue  
(1)change PR134 from 0 ohm to 47k ohm.  
(2)stuff C448 0.1uF

A1A:(10/18) Reserve .1uF

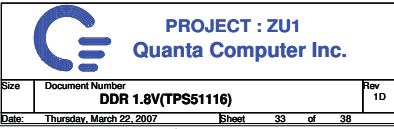
A1A:(10/2) change from +5VSUS to +5V\_S5

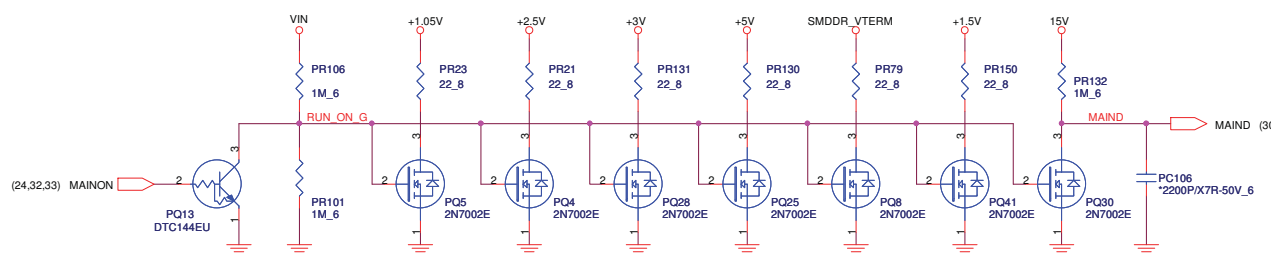
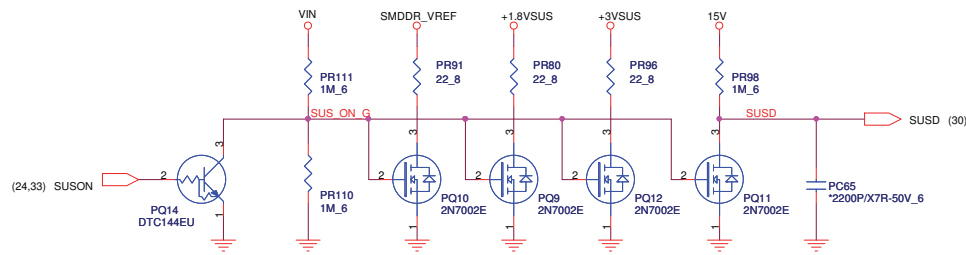
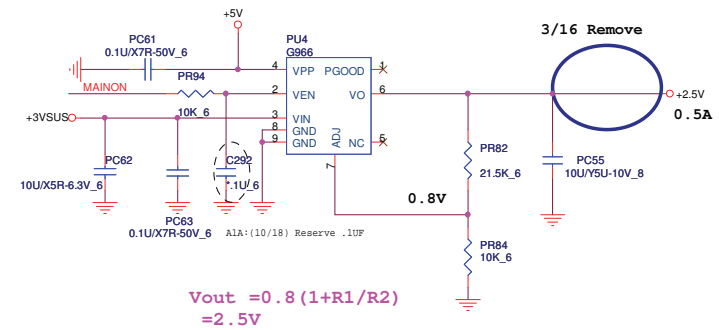
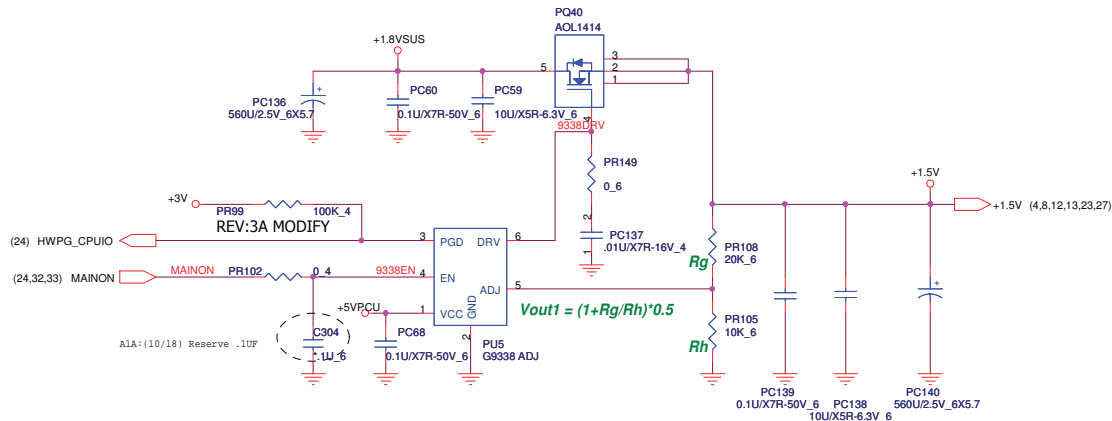
B1C:(11/29) Change PR8 from 20K(CS32003P933) to 6.65K ohm (CS26653P911)



PROJECT : ZU1  
Quanta Computer Inc.

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# 3/16 Modify PD9 Footprint from SBM1040-3P to SBM1040-3P-ZU1

